

Features

- Wide supply voltage range from 0.8V to 3.6V
- Inputs accept voltages up to 3.6V
- I_{OFF} supports partial-power-down mode
- Low static power consumption; I_{CC}=0.5μA (Max.)
- ESD Protection Exceeds JESD 22
 - 2000-V Human-Body Model (A114-A)
 - 200-V Machine Model (A115-A)
 - 1000-V Charged-Device Model (C101)

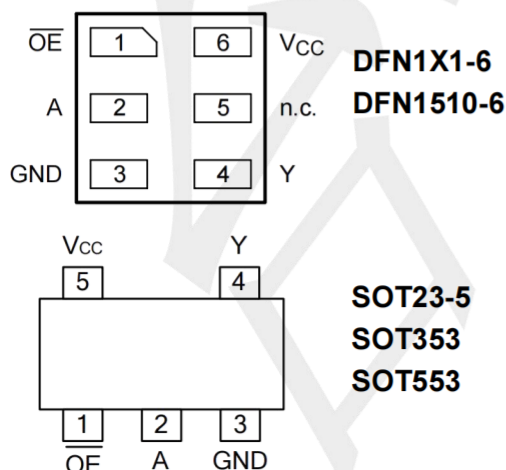
General Description

The TP74AUP1G240 is a 1-bit inverting buffer/line driver with 3-state outputs. The device features an output enable ($\overline{OE}$). A HIGH on $\overline{OE}$ causes the output to assume a high-impedance OFF-state. Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times.

Ordering Information

ORDER NUMBER	PACKAGE DESCRIPTION	PACKAGE OPTION
TP74AUP1G240S5	SOT23-5	Tape and Reel,3000
TP74AUP1G240C5	SOT353	Tape and Reel,3000
TP74AUP1G240X5	SOT553	Tape and Reel,4000
TP74AUP1G240D6	DFN1X1-6	Tape and Reel,5000
TP74AUP1G240N6	DFN1510-6	Tape and Reel,5000

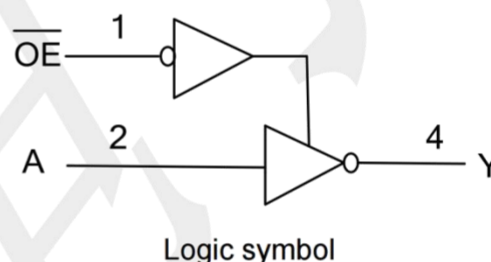
Pin Configuratio (TOP VIEW)



Applications

- AV Receiver
- Audio Dock:Portable
- Blu-ray Player and Home Theater
- Embedded PC
- Personal Digital Assistant(PDA)
- Power:Telecom/Server AC/DC Supply:Single Controller:Analog and Digital
- Solid State Drive(SSD):Client and Enterprise
- Wireless Headset,Keyboard,and Mouse

Logic Diagram



Function Table (each gate)

INPUT($\overline{OE}$)	INPUT(A)	OUTPUT(Y)
L	H	L
L	L	H
H	X	Z

H: HIGH voltage level; L: LOW voltage level;
X=don't care; Z=high-impedance OFF-state.

Absolute Maximum Ratings

PARAMETER	SYMBOL	CONDITIONS	RATINGS	UNIT
Supply Voltage	V _{CC}		-0.5 ~ +4.6	V
Input Voltage	V _{IN}		-0.5 ~ +4.6	V
Output Voltage	V _{OUT}	Output in the high or low state	-0.5 ~ +V _{CC} +0.5V	V
		Output in the power-off state	-0.5 ~ +4.6	V
VCC or GND Current	I _{CC}		±50	mA
Continuous Output Current	I _{OUT}	V _{OUT} =0~V _{CC}	±20	mA
Input Clamp Current	I _{IK}	V _{IN} <0	-50	mA
Output Clamp Current	I _{OK}	V _{OUT} <0	-50	mA
Storage Temperature Range	T _{STG}		-65 ~ +150	°C
Junction to Ambient	θ _{JA}	SOT-23-5	230	°C/W
		SOT353	280	°C/W
		SOT553	250	°C/W
		DFN1X1-6	460	°C/W
		DFN1510-6	440	°C/W

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

Recommended Operating Conditions

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V _{CC}	Operating	0.8	--	3.6	V
Input Voltage	V _{IN}		0	--	3.6	V
Output Voltage	V _{OUT}	High or low state	0	--	V _{CC}	V
Input Transition Rise or Fall Rate	Δt/Δv	V _{CC} =0.8V ~ 3.6V	--	--	200	ns/V
Operating Temperature	T _A		-40	--	+125	°C

TP74AUP1G240 Series

inverting buffer/line driver; 3-state

WWW.TECHPUBLIC.COM

Electrical Characteristics (T_A=25°C ,unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
High-level Input Voltage	V _{IH}	V _{CC} =0.8V	0.7×V _{CC}	--	--	V
		V _{CC} =0.9V ~ 1.95V	0.65×V _{CC}	--	--	V
		V _{CC} =2.3V ~ 2.7V	1.6	--	--	V
		V _{CC} =3V ~ 3.6V	2	--	--	V
Low-level Input Voltage	V _{IL}	V _{CC} =0.8V	--	--	0.3×V _{CC}	V
		V _{CC} =1.1V ~ 1.95V	--	--	0.35×V _{CC}	V
		V _{CC} =2.3V ~ 2.7V	--	--	0.7	V
		V _{CC} =3V ~ 3.6V	--	--	0.9	V
High-Level Output Voltage	V _{OH}	V _{CC} =0.8 ~ 3.6V, I _{OH} =-20μA	V _{CC} -0.1	--	--	V
		V _{CC} =1.1V, I _{OH} =-1.1mA	0.75×V _{CC}	--	--	V
		V _{CC} =1.4V, I _{OH} =-1.7mA	1.11	--	--	V
		V _{CC} =1.65V, I _{OH} =-1.9mA	1.32	--	--	V
		V _{CC} =2.3V	I _{OH} =-2.3mA	2.05	--	V
			I _{OH} =-3.1mA	1.9	--	V
		V _{CC} =3V	I _{OH} =-2.7mA	2.72	--	V
			I _{OH} =-4mA	2.6	--	V
Low-Level Output Voltage	V _{OL}	V _{CC} =0.8 ~ 3.6V, I _{OL} =20μA	--	--	0.1	V
		V _{CC} =1.1V, I _{OL} =1.1mA	--	--	0.3×V _{CC}	V
		V _{CC} =1.4V, I _{OL} =1.7mA	--	--	0.31	V
		V _{CC} =1.65V, I _{OL} =1.9mA	--	--	0.31	V
		V _{CC} =2.3V	I _{OL} =2.3mA	--	0.31	V
			I _{OL} =3.1mA	--	0.44	V
		V _{CC} =3V	I _{OL} =2.7mA	--	0.31	V
			I _{OL} =4mA	--	0.44	V
Input Leakage Current	I _{I(LEAK)}	V _{CC} =0 ~ 3.6V, V _{IN} =GND ~ 3.6V	--	--	±0.1	μA
Power OFF Leakage Current	I _{off}	V _{CC} =0 V, V _{IN} or V _{OUT} =0 ~ 3.6V	--	--	±0.2	μA
Additional Power OFF Leakage Current	ΔI _{off}	V _{CC} =0 V~0.2V, V _{IN} or V _{OUT} =0 ~ 3.6V	--	--	±0.2	μA
Quiescent Supply Current	I _{CC}	V _{CC} =0.8 ~3.6V, V _{IN} =V _{CC} or GND, I _{OUT} =0	--	--	0.5	μA
Additional Quiescent Supply Current Per Input Pin	ΔI _{CC}	V _{CC} =3.3 V, V _{IN} =V _{CC} -0.6V, I _{OUT} =0	--	--	40	μA
Input Capacitance	C _I	V _{CC} =0V-3.6V, V _{IN} =V _{CC} or GND	--	0.8	--	pF
Output Capacitance	C _{OUT}	V _{CC} =0V, V _{OUT} =GND	--	1.7	--	pF

TP74AUP1G240 Series

inverting buffer/line driver; 3-state

WWW.TECHPUBLIC.COM

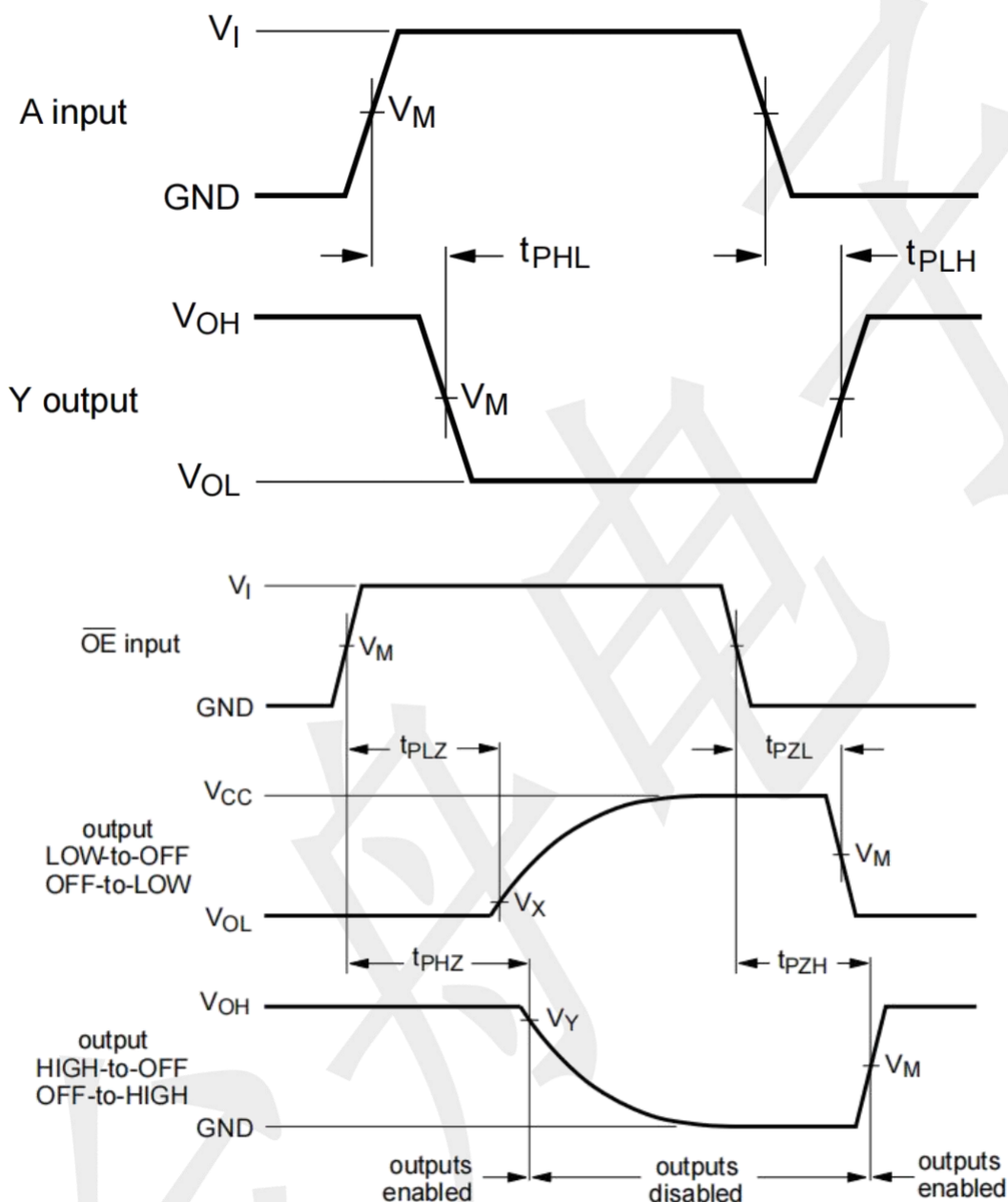
OPERATING CHARACTERISTICS (f=10MHz, TA =25°C , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Dissipation Capacitance	CPD	V _{CC} =0.8V	--	2.7	--	pF
		V _{CC} =1.2±0.1V	--	2.9	--	pF
		V _{CC} =1.5±0.1V	--	2.0	--	pF
		V _{CC} =1.8±0.15V	--	3.2	--	pF
		V _{CC} =2.5±0.2V	--	3.7	--	pF
		V _{CC} =3.3±0.3V	--	4.2	--	pF

SWITCHING CHARACTERISTICS (TA =25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Propagation delay from inputs (A) to output(Y)	t _{PD}	CL=5pF, RL=1MΩ	V _{CC} =0.8V	--	22.3	--	ns
			V _{CC} =1.2±0.1V	3.0	5.8	12.6	ns
			V _{CC} =1.5±0.1V	2.3	4.0	7.3	ns
			V _{CC} =1.8±0.15V	2.0	3.2	5.5	ns
			V _{CC} =2.5±0.2V	1.8	2.6	4.1	ns
			V _{CC} =3.3±0.3V	1.4	2.3	3.6	ns
		CL=10pF, RL=1MΩ	V _{CC} =0.8V	--	25.7	--	ns
			V _{CC} =1.2±0.1V	3.2	6.4	12.3	ns
			V _{CC} =1.5±0.1V	2.1	4.5	7.3	ns
			V _{CC} =1.8±0.15V	1.9	3.8	5.5	ns
			V _{CC} =2.5±0.2V	2.1	3.2	4.2	ns
			V _{CC} =3.3±0.3V	1.8	3.0	3.8	ns
		CL=15pF, RL=1MΩ	V _{CC} =0.8V	--	29.0	--	ns
			V _{CC} =1.2±0.1V	3.6	7.2	14.1	ns
			V _{CC} =1.5±0.1V	3.0	5.1	8.1	ns
			V _{CC} =1.8±0.15V	2.2	4.3	6.3	ns
			V _{CC} =2.5±0.2V	2.0	3.7	4.9	ns
			V _{CC} =3.3±0.3V	2.0	3.5	4.4	ns
		CL=30pF, RL=1MΩ	V _{CC} =0.8V	--	39.1	--	ns
			V _{CC} =1.2±0.1V	4.8	9.5	19	ns
			V _{CC} =1.5±0.1V	4.0	6.7	10.8	ns
			V _{CC} =1.8±0.15V	2.9	5.6	8.4	ns
			V _{CC} =2.5±0.2V	2.7	4.8	6.3	ns
			V _{CC} =3.3±0.3V	2.7	4.6	5.8	ns

TEST CIRCUIT AND WAVEFORMS

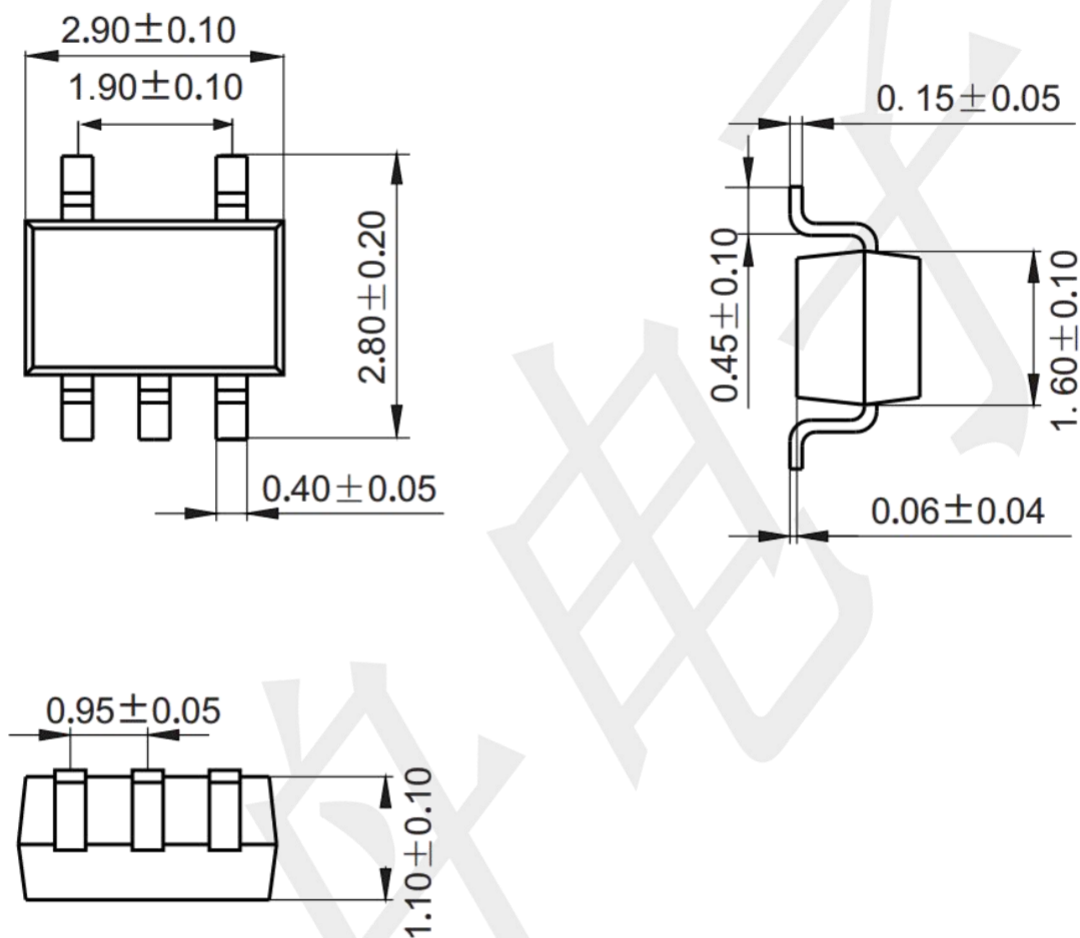


Notes:

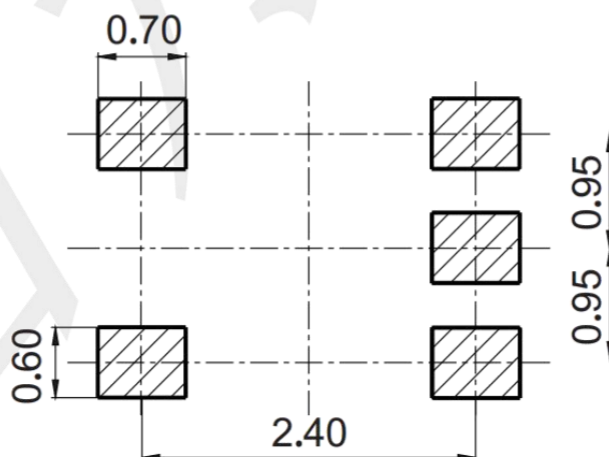
1. CL includes probe and jig capacitance.
2. All input pulses are supplied by generators having the following characteristics: PRR ≤ 10 MHz, $Z_O = 50\Omega$.
3. The outputs are measured one at a time, with one transition per measurement.
4. t_{PLH} and t_{PHL} are the same as t_{pd} .
5. All parameters and waveforms are not applicable to all devices

Package information (Unit: mm)

SOT23-5

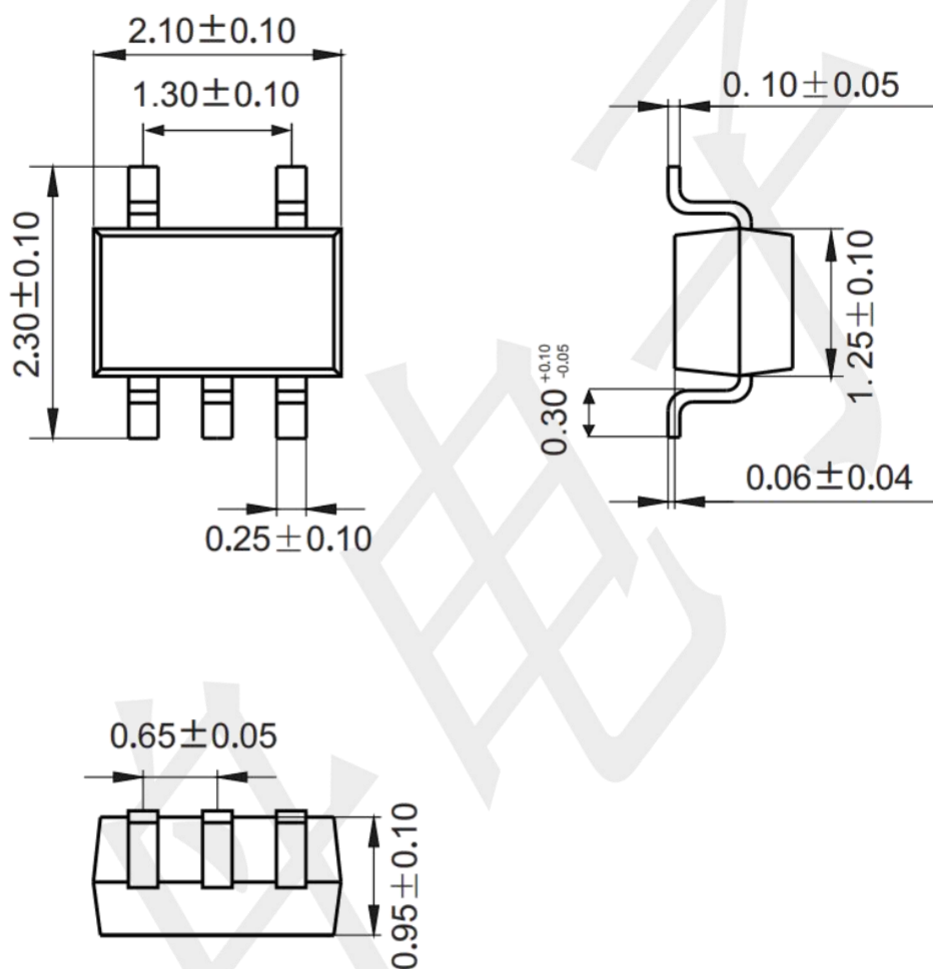


Mounting Pad Layout (unit: mm)

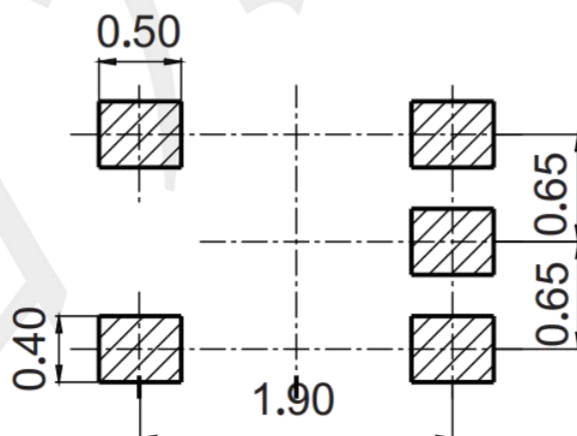


Package information (Unit: mm)

SOT353

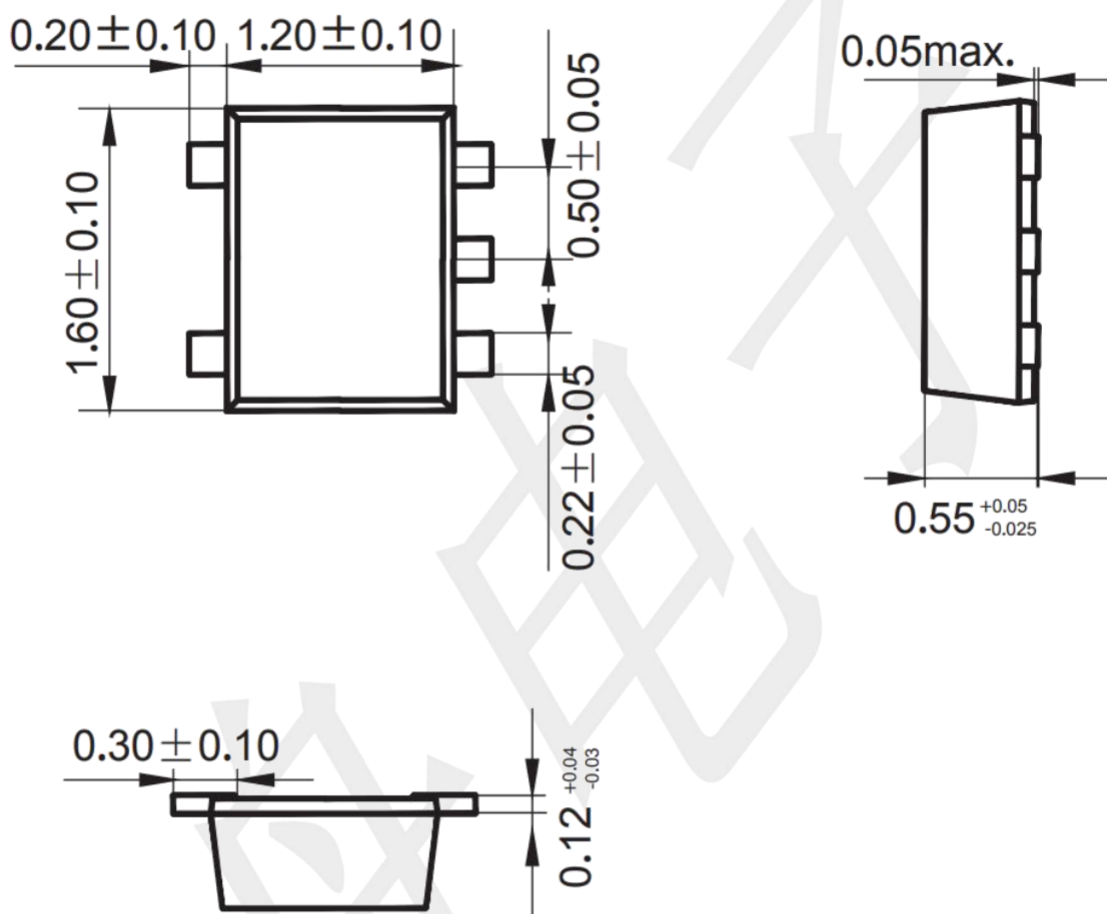


Mounting Pad Layout (unit: mm)

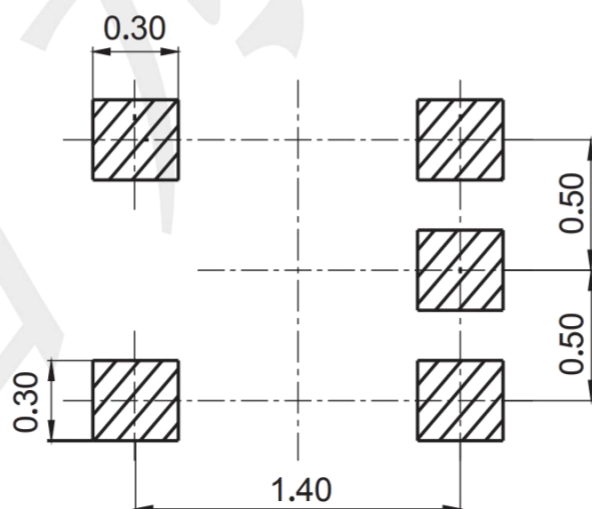


Package information (unit: mm)

SOT553

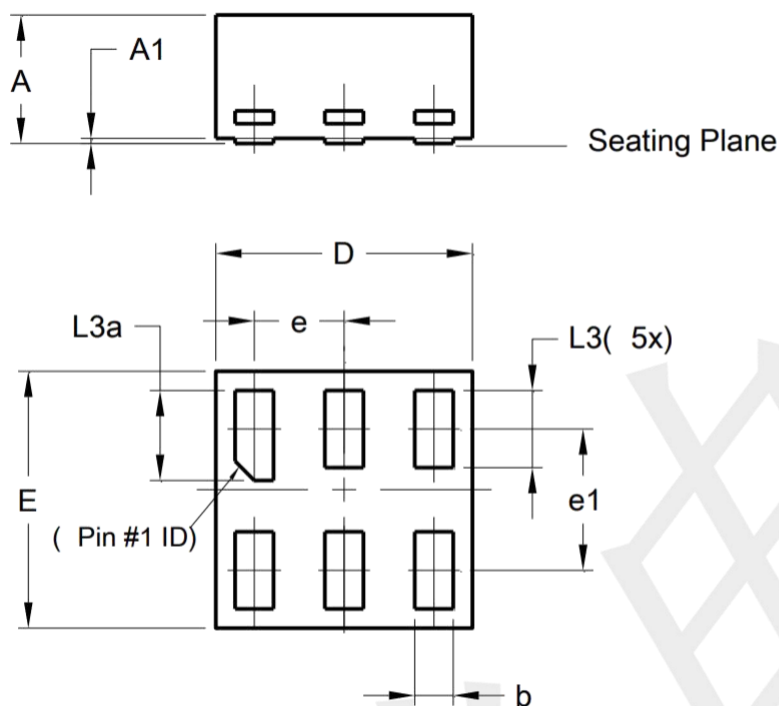


Mounting Pad Layout (unit: mm)



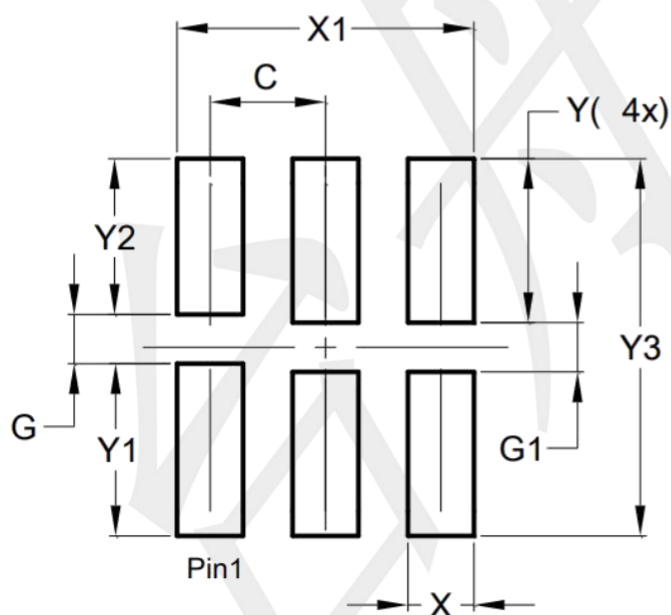
Package information (unit: mm)

DFN1X1-6



DFN1010-6 (Type B)			
Dim	Min	Max	Typ
A	-	0.50	0.39
A1	-	0.04	-
b	0.12	0.20	0.15
D	0.95	1.050	1.00
E	0.95	1.050	1.00
e	0.35 BSC		
e1	0.55 BSC		
L3	0.27	0.30	0.30
L3a	0.32	0.40	0.35
All Dimensions in mm			

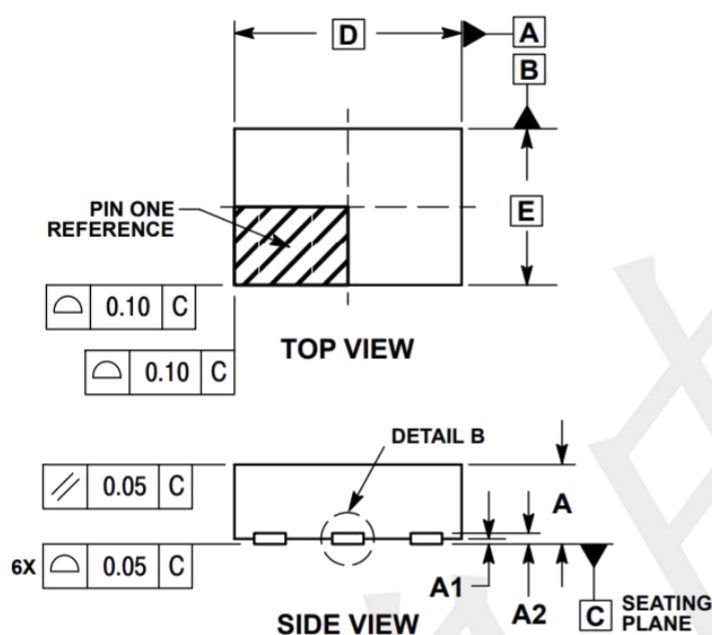
Mounting Pad Layout (unit: mm)



Dimensions	Value (in mm)
C	0.350
G	0.150
G1	0.150
X	0.200
X1	0.900
Y	0.500
Y1	0.525
Y2	0.475
Y3	1.150

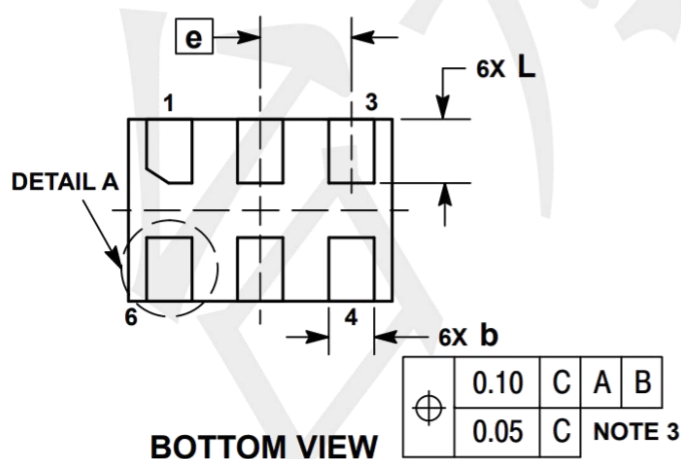
Package information (unit: mm)

DFN1510-6



DIM	MILLIMETERS	
	MIN	MAX
A	0.45	0.55
A1	0.00	0.05
A2	0.07 REF	
b	0.20	0.30
D	1.45 BSC	
E	1.00 BSC	
e	0.50 BSC	
L	0.30	0.40
L1	---	0.15

Mounting Pad Layout (unit: mm)



MOUNTING FOOTPRINT

