

● General Description

The AGM40P20S combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$. This device is ideal for load switch and battery protection applications.

● Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

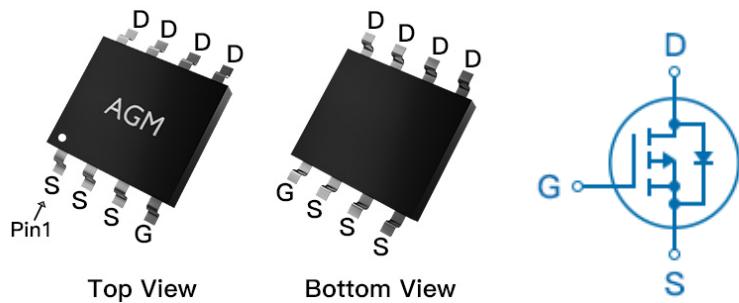
● Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDS(on)	ID
-40V	28mΩ	-8.0A

SOP8 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM40P20S	AGM40P20S	SOP8	330mm	12mm	3000

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	-40	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Ta=25°C) (Note 1)	-8.0	A
	Drain Current-Continuous(Ta=100°C)	-5.4	A
IDM (pulse)	Drain Current-Pulsed (Note 2)	32	A
PD	Maximum Power Dissipation(Ta=25°C)	2.5	W
	Maximum Power Dissipation(Ta=100°C)	1.0	W
EAS	Avalanche energy (Note 3)	41	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
R _{θJA}	Thermal Resistance Junction-ambient (Steady State) ¹	---	50	°C/W

Table 3. Electrical Characteristics (TJ=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=-250μA	-40	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=-40V, VGS=0V	--	--	-1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V, VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=-250μA	-1.2	-1.6	-2.2	V
gFS	Forward Transconductance	VDS=-5V, ID=-5A	--	10	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=-10V, ID=-8A	--	28	35	mΩ
		VGS=-4.5V, ID=-5A	--	36	45	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=-20V, VGS=0V, F=1MHZ	--	1200	--	pF
Coss	Output Capacitance		--	120	--	pF
Crss	Reverse Transfer Capacitance		--	107	--	pF
Rg	Gate resistance	f=1.0MHz	--	3.0	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=-10V, VDS=-15V, ID=-5A, RGEN=3.3Ω	--	22	--	nS
tr	Turn-on Rise Time		--	15	--	nS
td(off)	Turn-Off Delay Time		--	59.8	--	nS
tf	Turn-Off Fall Time		--	5.4	--	nS
Qg	Total Gate Charge	VGS=-10V, VDS=-25V, ID=-5A	--	11	--	nC
Qgs	Gate-Source Charge		--	3.5	--	nC
Qgd	Gate-Drain Charge		--	3.2	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	-8.0	A
VSD	Forward on Voltage	VGS=0V, IS=-8A	--	--	-1.2	V
trr	Reverse Recovery Time	Isd=-8A, dI/dt=100A/μs, TJ=25°C	--	--	--	ns
Qrr	Reverse Recovery Charge		--	--	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C

Typical Characteristics

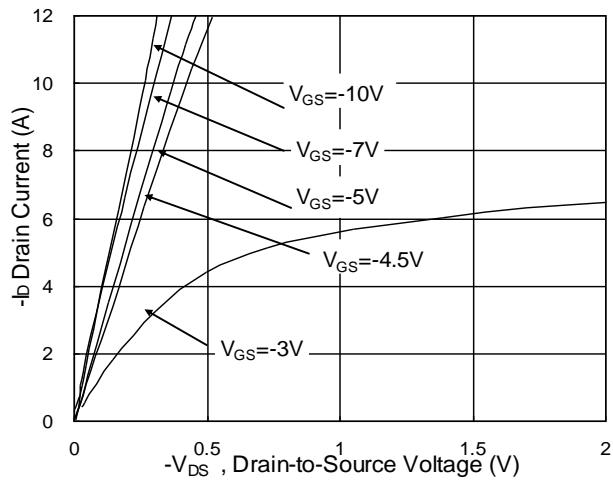


Fig.1 Typical Output Characteristics

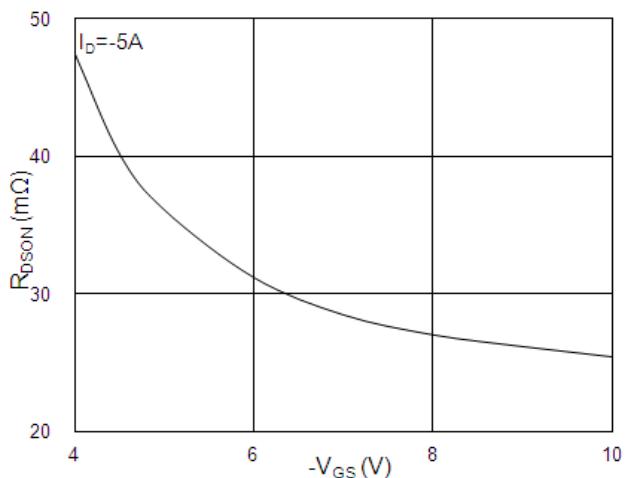


Fig.2 On-Resistance vs G-S Voltage

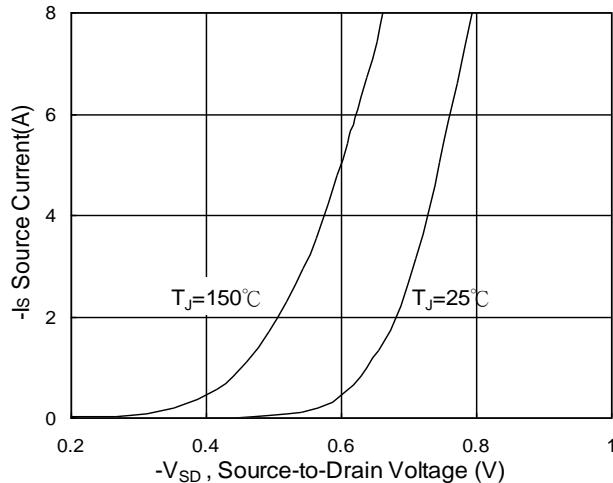


Fig.3 Source Drain Forward Characteristics

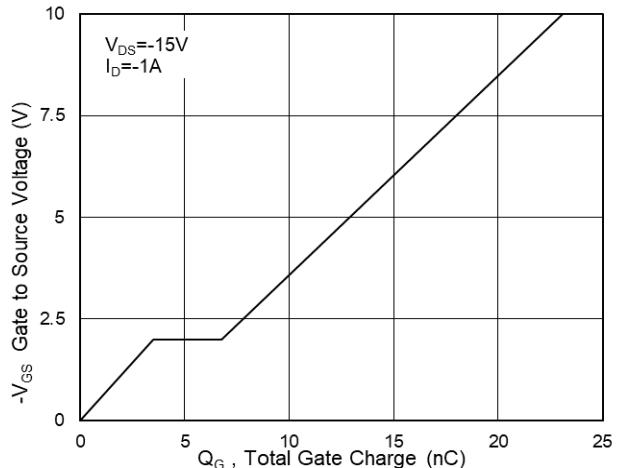


Fig.4 Gate Charge Characteristics

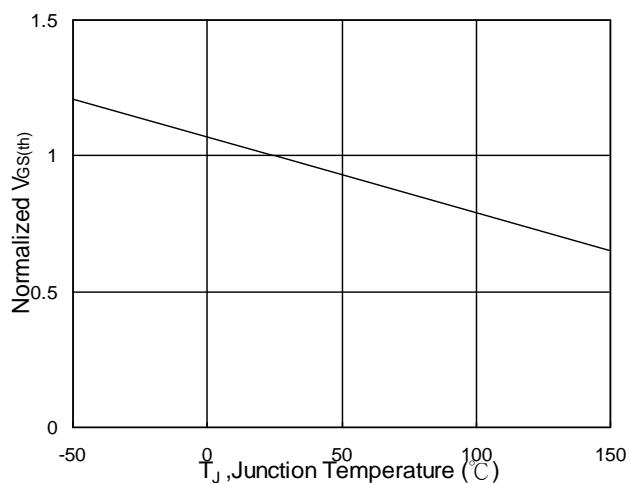


Fig.5 Normalized $V_{GS(th)}$ vs T_J

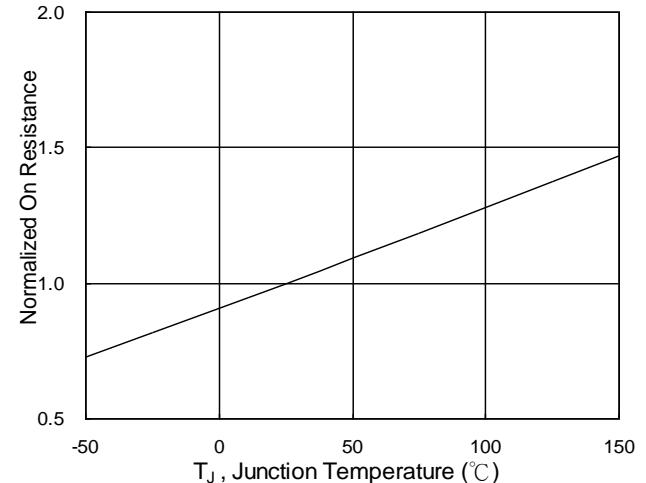
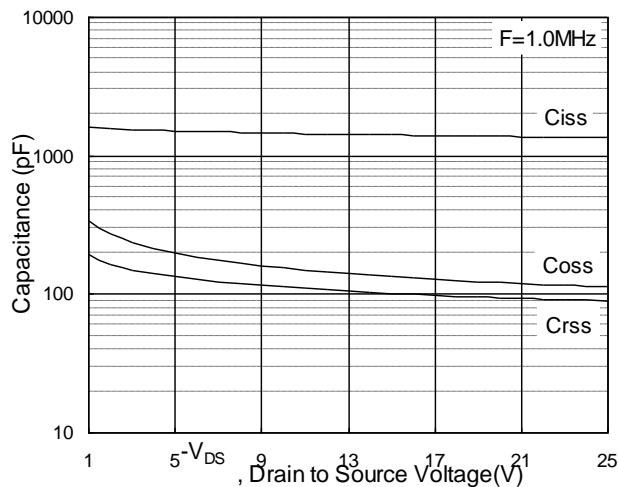
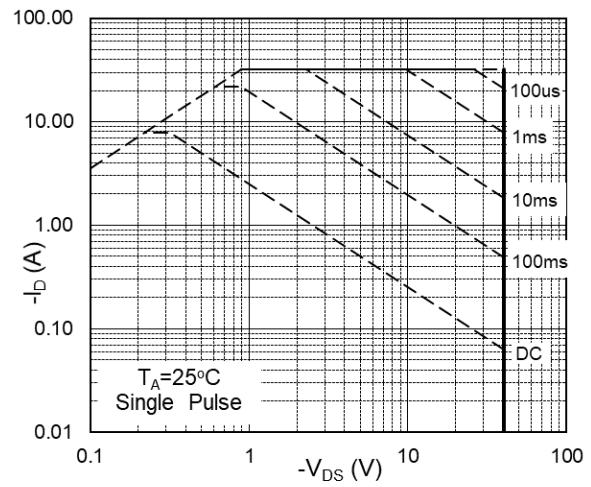
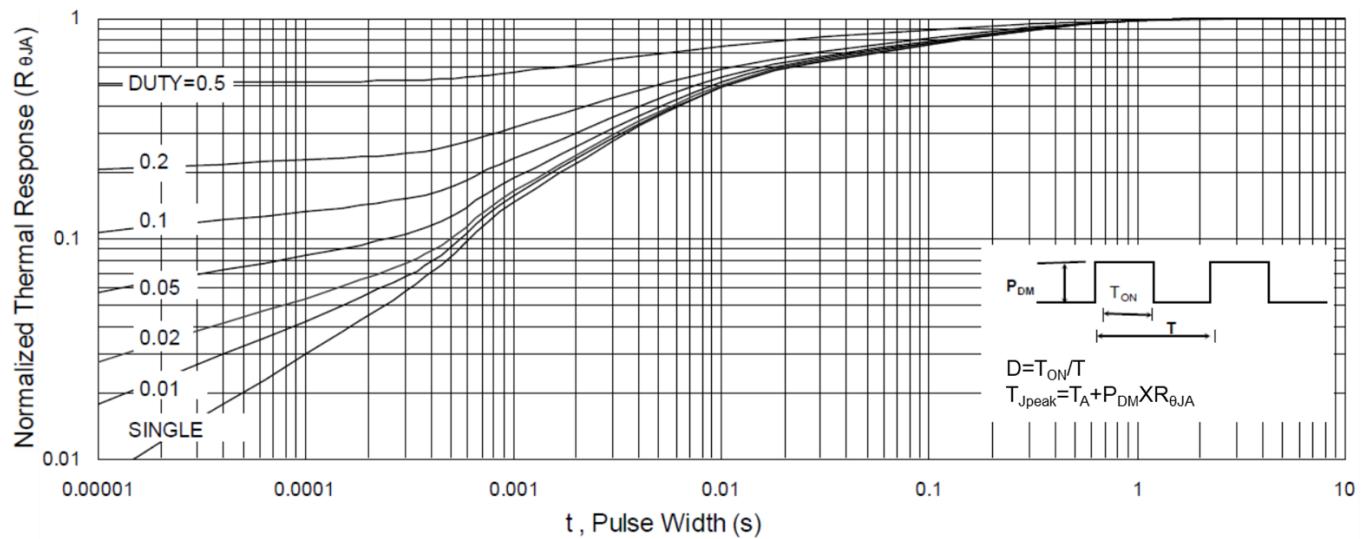
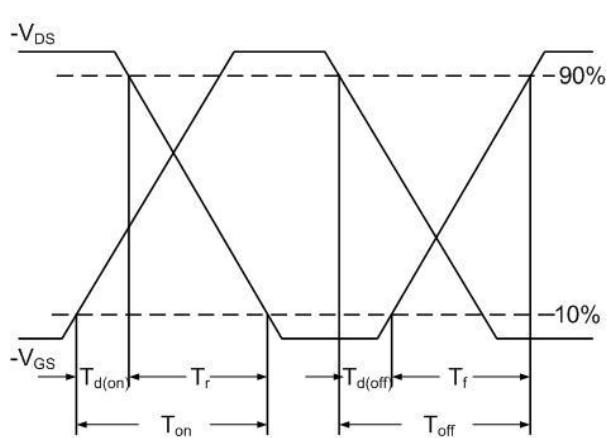
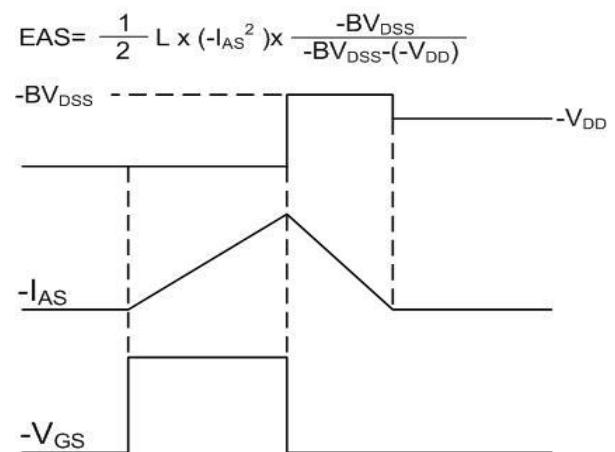
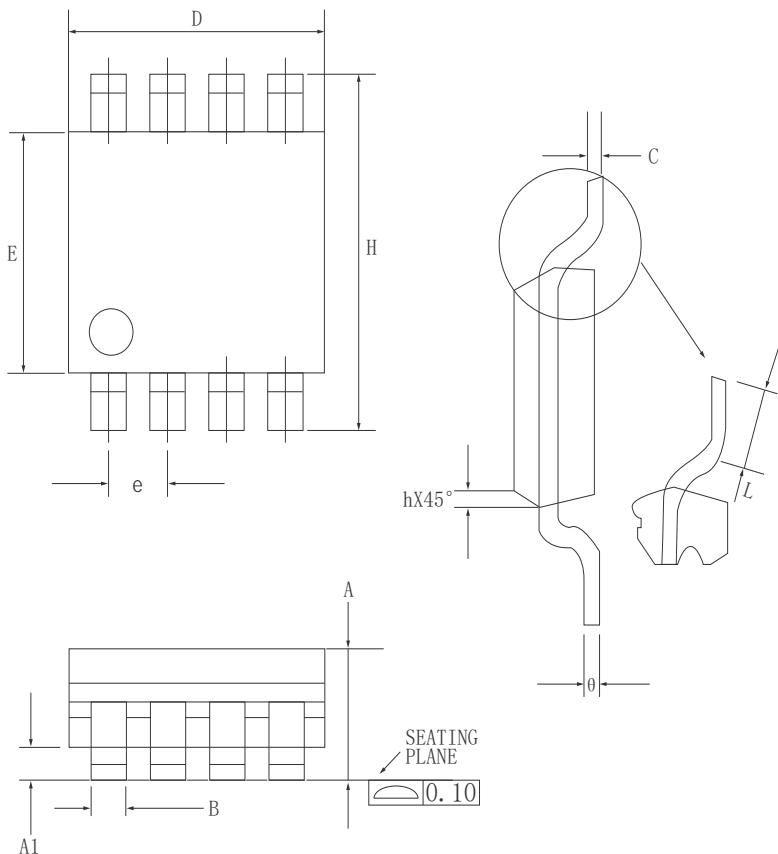


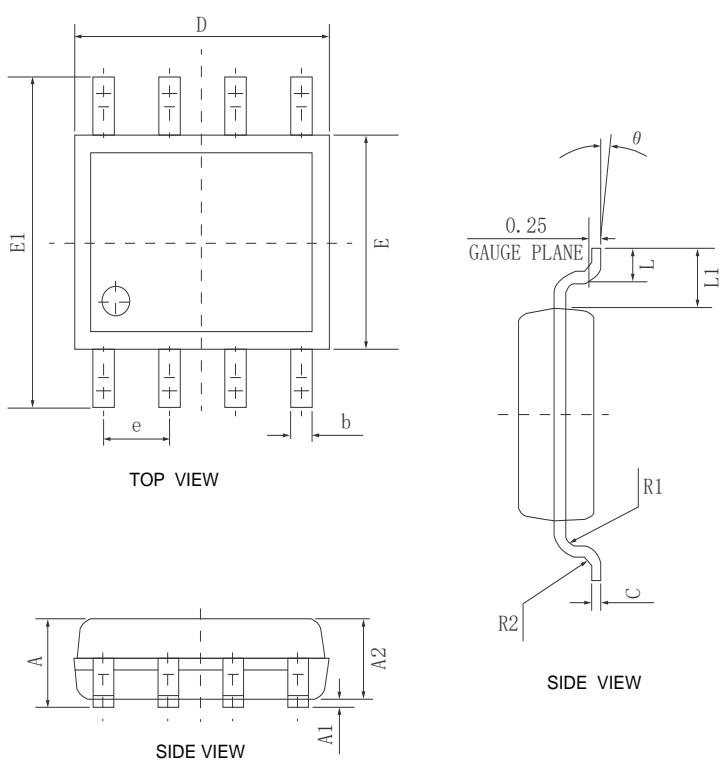
Fig.6 Normalized $R_{DS(on)}$ vs T_J


Fig. 7 Capacitance

Fig. 8 Safe Operating Area

Fig. 9 Normalized Maximum Transient Thermal Impedance

Fig. 10 Switching Time Waveform

Fig. 11 Unclamped Inductive Switching Waveform

•Dimensions (SOP8)



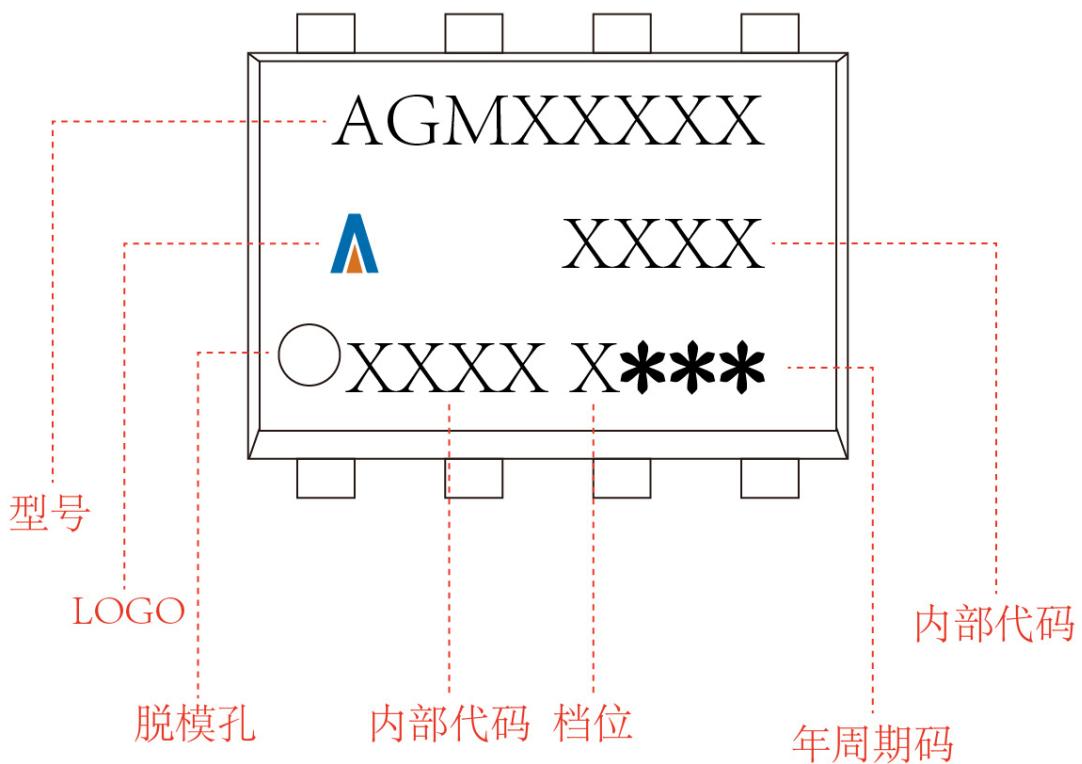
DIM	MILLIMETERS	
	MIN	MAX
A	1.35	1.75
A1	0.02	0.15
B	0.33	0.5
C	0.1	0.25
D	4.8	5
E	3.8	4
e	1.27 (BSC)	
H	5.8	6.2
h	0.25	0.5
I	0.4	1.25
θ	0°	7°



SYMBOL	MIN	NOM	MAX
A	1.40	1.60	1.80
A1	0.05	0.15	0.25
A2	1.35	1.45	1.55
b	0.30	0.40	0.50
c	0.153	0.203	0.253
D	4.80	4.90	5.00
E	3.80	3.90	4.00
E1	5.80	6.00	6.20
L	0.45	0.70	1.00
θ	2°	4°	6°
L1	1.04 REF		
e	1.27 BSC		
R1	0.07 TYP		
R2	0.07 TYP		

SOP8

Marking Instructions:



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