

**N-Ch MOSFET** 

### **General Description**

The WSD80120DN56 is the highest performance trench N-Ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications . The WSD80120DN56 meet the RoHS and Green Product requirement,100% EAS guaranteed with full function reliability approved.

#### **Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

### **Product Summery**

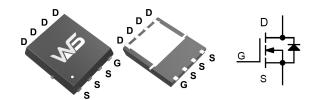
BV <sub>DSS</sub>	R <sub>DSON</sub>	I <sub>D</sub>
85V	$3.7 m\Omega$	120A

### **Applications**

High power DC/DC converters and switch mode power supply

DC Motor control and Class D Amplifier

## **DFN5X6-8 Pin Configuration**



### **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	85	V
$V_{GS}$	Gate-Source Voltage	±25	V
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V	120	Α
I <sub>D</sub> @T <sub>C</sub> =100℃	Continuous Drain Current, V <sub>GS</sub> @ 10V	96	Α
I <sub>DM</sub>	Pulsed Drain Current T <sub>C</sub> =25°C	384	Α
EAS	Avalanche Energy, Single pulse,L=0.5mH	320	mJ
I <sub>AS</sub>	Avalanche Current, Single pulse,L=0.5mH	180	Α
P <sub>D</sub> @T <sub>C</sub> =25℃	Total Power Dissipation	104	W
P <sub>D</sub> @T <sub>C</sub> =100℃	Total Power Dissipation	53	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 175	°C
TJ	Operating Junction Temperature Range	175	°C

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
$R_{ heta JA}$	Thermal Resistance Junction-Ambient		20	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case		1.2	°C/W



## Electrical Characteristics (T<sub>J</sub>=25 C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	85			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.096		V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V,I <sub>D</sub> =50A		3.7	4.8	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> . In =250uA	2.0	3.0	4.0	٧
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	VGS-VDS , ID -250UA		-5.5		mV/℃
	Drain-Source Leakage Current	$V_{DS}$ =85V , $V_{GS}$ =0V , $T_{J}$ =25 $^{\circ}$ C			1	uA
I <sub>DSS</sub>		$V_{DS}$ =85V , $V_{GS}$ =0V , $T_{J}$ =55 $^{\circ}$ C			10	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±25V , V <sub>DS</sub> =0V			±100	nA
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		3.2		Ω
$Q_{g}$	Total Gate Charge (10V)			54		nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =50V , V <sub>GS</sub> =10V , I <sub>D</sub> =10A		17		
$Q_gd$	Gate-Drain Charge			11		
T <sub>d(on)</sub>	Turn-On Delay Time			21		
Tr	Rise Time	V <sub>DD</sub> =50V , V <sub>GS</sub> =10V ,	, 18			
T <sub>d(off)</sub>	Turn-Off Delay Time	$R_G$ =1 $\Omega$ ,RL=1 $\Omega$ ,IDS=10A.		36		ns
T <sub>f</sub>	Fall Time			10		1
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =40V , V <sub>GS</sub> =0V , f=1MHz		3750		
C <sub>oss</sub>	Output Capacitance			395		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			180		

### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			86.7	Α
V <sub>SD</sub>	Diode Forward Voltage	$V_{GS}$ =0V , $I_{S}$ =15A , $T_{J}$ =25 $^{\circ}$ C			1.2	V

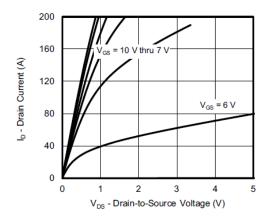
A: The value of R  $_{\theta}$  JA is measured with the device mounted on 1in $^2$  FR-4 board with 2oz. Copper, in a still air environment with TA=25 $^{\circ}$ C. The value in any given application depends on the user's specific board design.

B: Repetitive rating, pulse width limited by junction temperature.

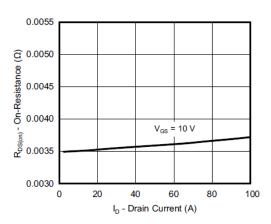
C: The current rating is based on the t≤ 10s junction to ambient thermal resistance rating.



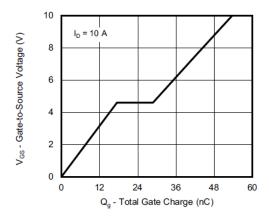
## **Typical Operating Characteristics**



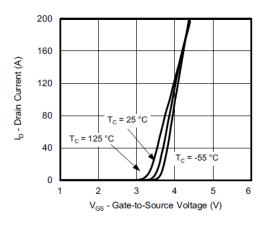
**Output Characteristics** 



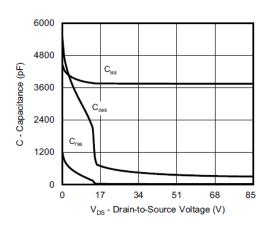
On-Resistance vs. Drain Current and Gate Voltage



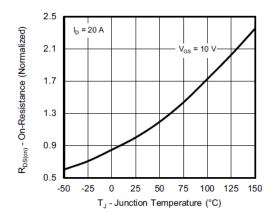
**Gate Charge** 



**Transfer Characteristics** 



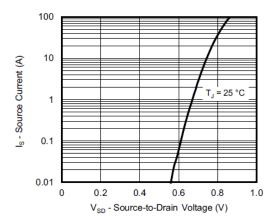
Capacitance



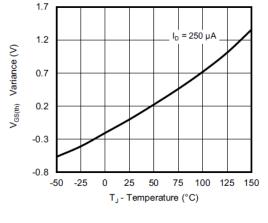
On-Resistance vs. Junction Temperature



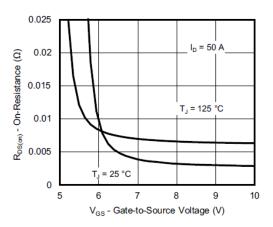
# **Typical Operating Characteristics**



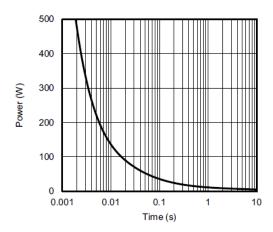
Source-Drain Diode Forward Voltage



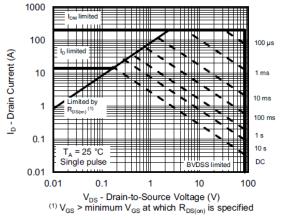
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



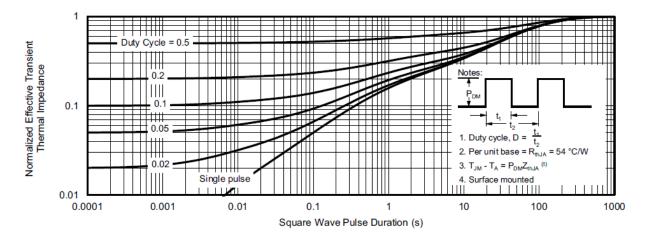
Single Pulse Power, Junction-to-Ambient



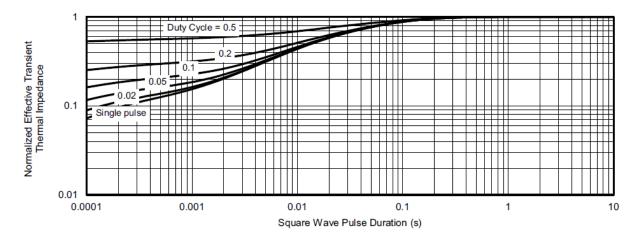
Safe Operating Area, Junction-to-Ambient



# **Typical Operating Characteristics**



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case



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