

#### **General Description**

The WSF40P03 is the highest performance trench P-Ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The WSF40P03 meet the RoHS and Green Product requirement with full function reliability approved.

#### **Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

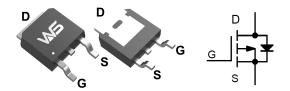
#### **Product Summery**

BVDSS	RDSON	ID
-30V	18mΩ	-40A

#### **Applications**

- Power Magagement in Desktop
- DC/DC Converters.
- Load Switch

#### **TO-252 Pin Configuration**



#### **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-30	V
$V_{GS}$	Gate-Source Voltage	±25	V
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ -10V	-40	Α
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ -10V	-25	Α
I <sub>D</sub> @T <sub>A</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ -10V	-9.6	Α
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ -10V	-7.7	Α
I <sub>DM</sub> <sup>a</sup>	Pulsed Drain Current	-160	Α
E <sub>AS</sub> <sup>b</sup>	Single Pulse Avalanche Energy	28	mJ
I <sub>AS</sub> b	Avalanche Current	-20	Α
P <sub>D</sub> @T <sub>A</sub> =25℃	Total Power Dissipation	2.5	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}^{c}$	Thermal Resistance Junction-Ambient		50	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case		2.9	°C/W

Note a : Pulse width limited by max. junction temperature.

Note b : UIS tested and pulse width limited by maximum junction temperature  $150^{\circ}$ C (initial temperature  $T_i = 25^{\circ}$ C).

Note c : Surface Mounted on 1in² pad area.



### Electrical Characteristics (T<sub>J</sub>=25 C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-30			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25 $^{\circ}\mathbb{C}$ , ID=-1mA		-0.022		V/°C
R <sub>DS(ON)</sub> d	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-10V , I <sub>D</sub> =-20A		15	18	mΩ
		V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-15A		20	26	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	-1.2	-1.6	-2.5	V
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS}$ =-24V , $V_{GS}$ =0V , $T_J$ =25 $^{\circ}$ C			-1	- uA
		V <sub>DS</sub> =-24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			-5	
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = $\pm 20 V$ , $V_{DS}$ = $0 V$			±100	nA
$R_g$	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		3.5		Ω
$Q_g$	Total Gate Charge	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-4.5V, L <sub>DS</sub> =-20A		11		nC
Q <sub>gs</sub>	Gate-Source Charge			5		
Q <sub>gd</sub>	Gate-Drain Charge			5		
T <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD}$ =-15V, $R_{L}$ =15 $\Omega$ , $I_{DS}$ =-1A, $V_{GEN}$ =-10V, $R_{G}$ =6 $\Omega$		11	20	
Tr	Rise Time			10	18	
T <sub>d(off)</sub>	Turn-Off Delay Time			39	70	ns
T <sub>f</sub>	Fall Time			29	53	
C <sub>iss</sub> e	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =-15V, Frequency=1.0MHz		1256	1633	
C <sub>oss</sub> e	Output Capacitance			187		pF
C <sub>rss</sub> <sup>e</sup>	Reverse Transfer Capacitance			115		

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			-20	Α
V <sub>SD</sub> <sup>d</sup>	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A			-1.2	V
t <sub>rr</sub>	Reverse Recovery Time	IF=-20A , dI/dt=100A/μs		12		nS
Q <sub>rr</sub>	Reverse Recovery Charge			3.5		nC

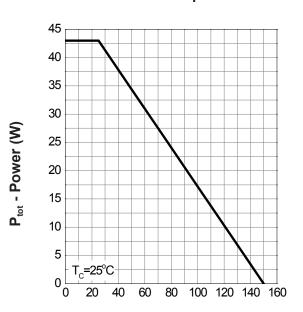
Note d : Pulse test ; pulse width≤300 $\mu$ s, duty cycle≤2%.

Note e: Guaranteed by design, not subject to production testing.



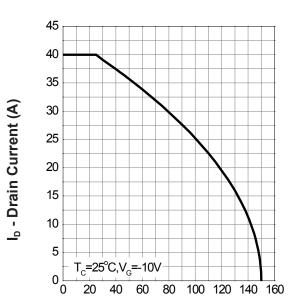
## **Typical Operating Characteristics**

# Power Dissipation



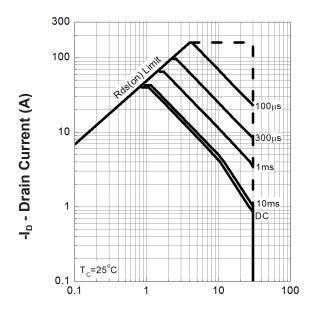
T<sub>i</sub> - Junction Temperature (°C)

#### **Drain Current**



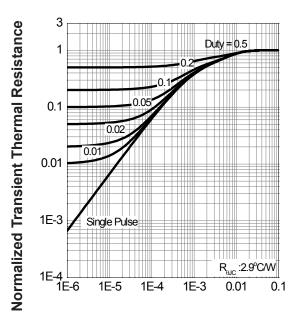
T<sub>i</sub> - Junction Temperature (°C)

#### **Safe Operation Area**



-V<sub>DS</sub> - Drain - Source Voltage (V)

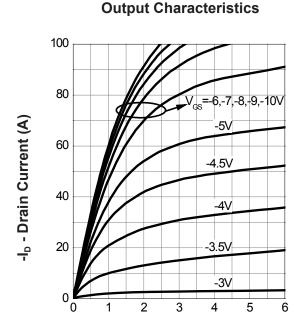
#### **Thermal Transient Impedance**



**Square Wave Pulse Duration (sec)** 

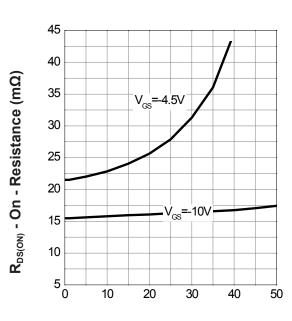


## **Typical Operating Characteristics(Cont.)**



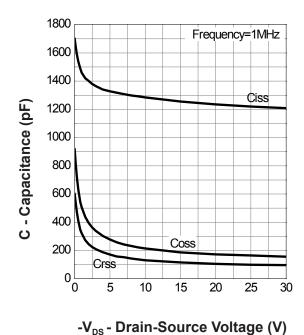
-V<sub>DS</sub> - Drain - Source Voltage (V)

#### **Drain-Source On Resistance**

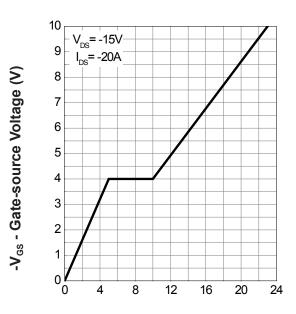


-I<sub>D</sub> - Drain Current (A)

#### Capacitance



#### **Gate Charge**

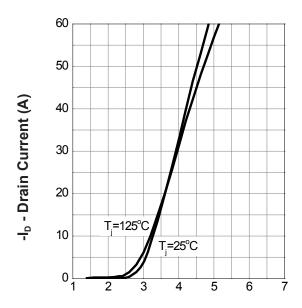


Q<sub>G</sub> - Gate Charge (nC)



## **Typical Operating Characteristics(Cont.)**

#### **Transfer Characteristics**



-V<sub>GS</sub> - Gate-Source Voltage (V)



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