



# 7N65-ML

Power MOSFET

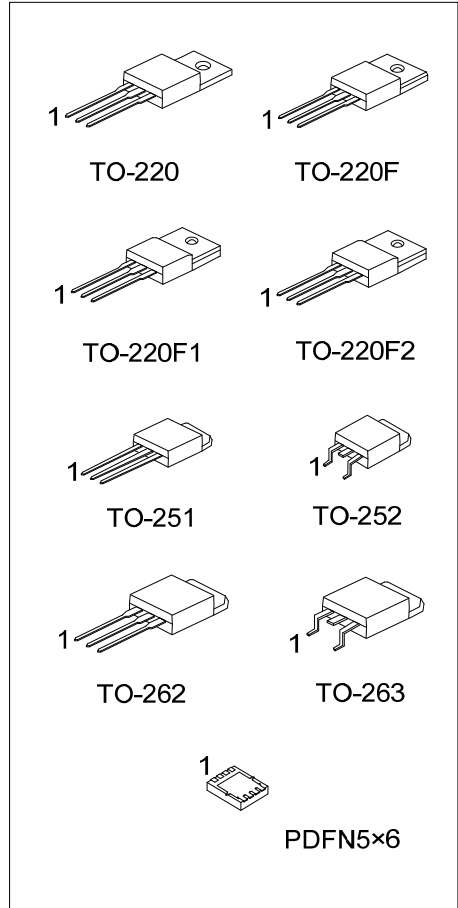
## 7.0A, 650V N-CHANNEL POWER MOSFET

### DESCRIPTION

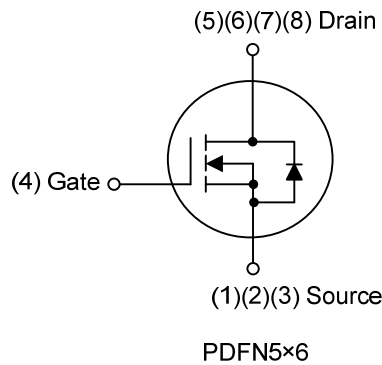
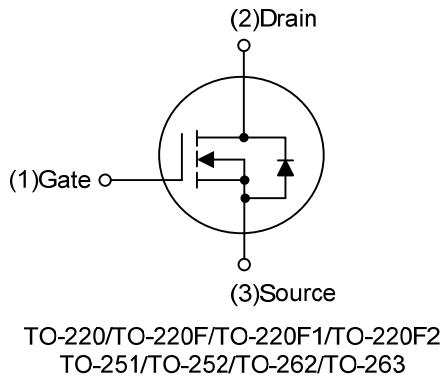
The UTC **7N65-ML** is a high voltage power MOSFET combines advanced planar MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

### FEATURES

- \*  $R_{DS(ON)} \leq 1.3 \Omega @ V_{GS}=10V, I_D=3.5A$
- \* Fast switching capability
- \* Avalanche energy tested
- \* Improved dv/dt capability, high ruggedness



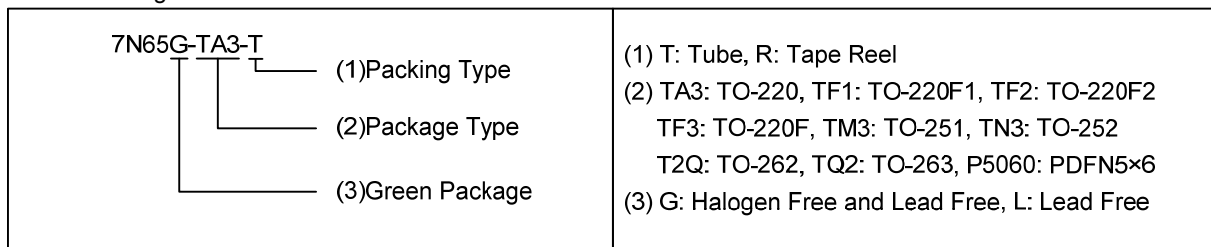
### SYMBOL



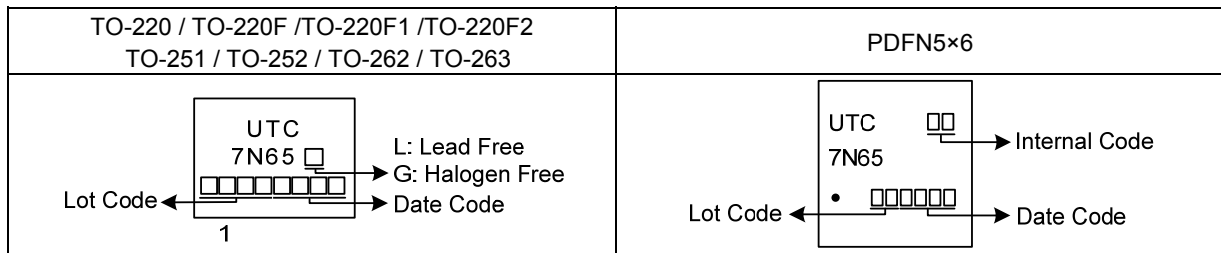
## ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
7N65L-TA3-T	7N65G-TA3-T	TO-220	G	D	S	-	-	-	-	-	Tube
7N65L-TF1-T	7N65G-TF1-T	TO-220F1	G	D	S	-	-	-	-	-	Tube
7N65L-TF2-T	7N65G-TF2-T	TO-220F2	G	D	S	-	-	-	-	-	Tube
7N65L-TF3-T	7N65G-TF3-T	TO-220F	G	D	S	-	-	-	-	-	Tube
7N65L-TM3-T	7N65G-TM3-T	TO-251	G	D	S	-	-	-	-	-	Tube
7N65L-TN3-R	7N65G-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
7N65L-T2Q-T	7N65G-T2Q-T	TO-262	G	D	S	-	-	-	-	-	Tube
7N65L-TQ2-T	7N65G-TQ2-T	TO-263	G	D	S	-	-	-	-	-	Tube
7N65L-TQ2-R	7N65G-TQ2-R	TO-263	G	D	S	-	-	-	-	-	Tape Reel
7N65L-P5060-R	7N65G-P5060-R	PDFN5×6	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source



## MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_c=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	650	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Continuous Drain Current		$I_D$	7	A
Pulsed Drain Current (Note 2)		$I_{DM}$	28	A
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	281.3	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.3	V/ns
Power Dissipation	TO-220/TO-262 TO-263	$P_D$	125	W
	TO-220F/TO-220F1 TO-220F2		35	W
	TO-251/TO-252		48	W
	PDFN5×6		140	W
Junction Temperature		$T_J$	+150	$^\circ\text{C}$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $L = 10\text{mH}$ ,  $I_{AS} = 7.5\text{A}$ ,  $V_{DD} = 50\text{V}$ ,  $R_G = 25\ \Omega$ , Starting  $T_J = 25^\circ\text{C}$

4.  $I_{SD} \leq 7.0\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2 TO-262/ TO-263	$\theta_{JA}$	62.5	$^\circ\text{C}/\text{W}$
	TO-251/TO-252		110	$^\circ\text{C}/\text{W}$
	PDFN5×6		35	$^\circ\text{C}/\text{W}$
	TO-220/TO-262 TO-263		1	$^\circ\text{C}/\text{W}$
Junction to Case	TO-220F/TO-220F1 TO-220F2	$\theta_{JC}$	3.57	$^\circ\text{C}/\text{W}$
	TO-251/TO-252		2.6 (Note)	$^\circ\text{C}/\text{W}$
	PDFN5×6		0.89	$^\circ\text{C}/\text{W}$

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

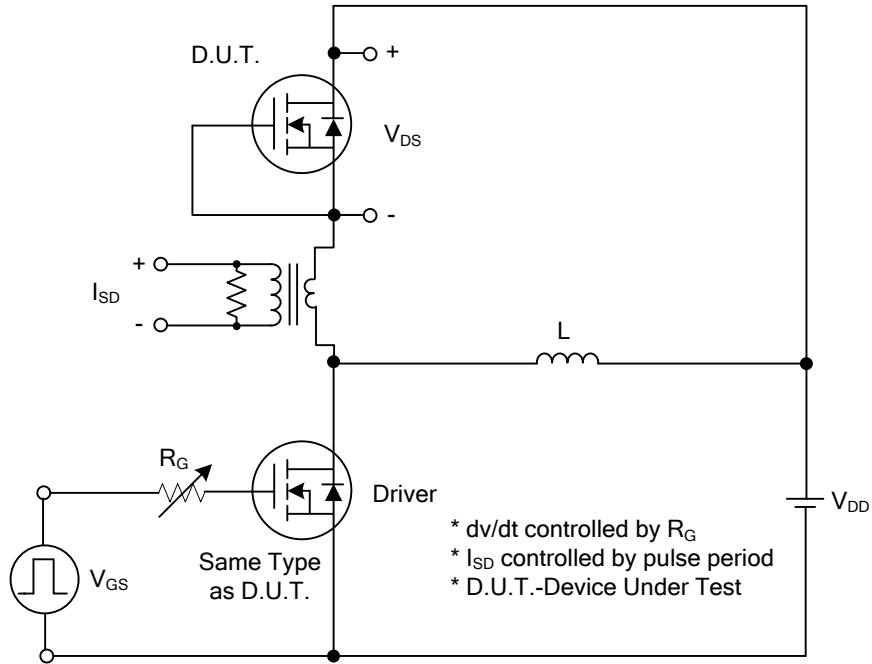
■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	650			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =650V, V <sub>GS</sub> =0V			10	μA
Gate- Source Leakage Current	Forward	I <sub>GSS</sub> V <sub>GS</sub> =30V, V <sub>DS</sub> =0V			100	nA
	Reverse		V <sub>GS</sub> =-30V, V <sub>DS</sub> =0V			-100
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0		4.0	V
Static Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =3.5A			1.3	Ω
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHz		870		pF
Output Capacitance	C <sub>OSS</sub>			97		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			9.6		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge (Note 1)	Q <sub>G</sub>	V <sub>DS</sub> =520V, V <sub>GS</sub> =10V, I <sub>D</sub> =7A I <sub>G</sub> =1mA (Note 1, 2)		22		nC
Gate-Source Charge	Q <sub>GS</sub>			5		nC
Gate-Drain Charge	Q <sub>GD</sub>			5.5		nC
Turn-On Delay Time (Note 1)	t <sub>D(ON)</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =10V, I <sub>D</sub> =7A, R <sub>G</sub> =25Ω (Note 1, 2)		12		ns
Turn-On Rise Time	t <sub>R</sub>			20		ns
Turn-Off Delay Time	t <sub>D(OFF)</sub>			74		ns
Turn-Off Fall Time	t <sub>F</sub>			33		ns
<b>DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS</b>						
Maximum Body-Diode Continuous Current	I <sub>S</sub>				7	A
Maximum Body-Diode Pulsed Current	I <sub>SM</sub>				28	A
Drain-Source Diode Forward Voltage (Note 1)	V <sub>SD</sub>	I <sub>S</sub> =7A, V <sub>GS</sub> =0V			1.4	V
Reverse Recovery Time (Note 1)	t <sub>rr</sub>	I <sub>S</sub> =7A, V <sub>GS</sub> =0V di/dt=100A/μs		506		ns
Reverse Recovery Charge	Q <sub>rr</sub>				2.7	

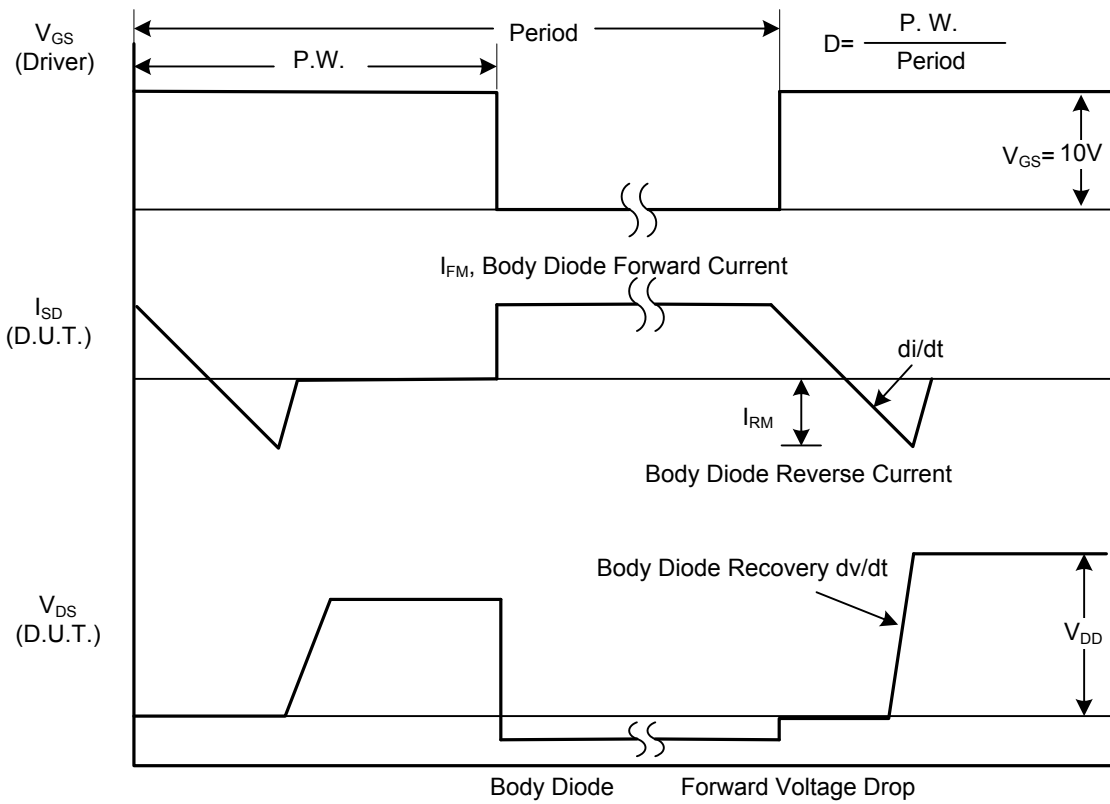
Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

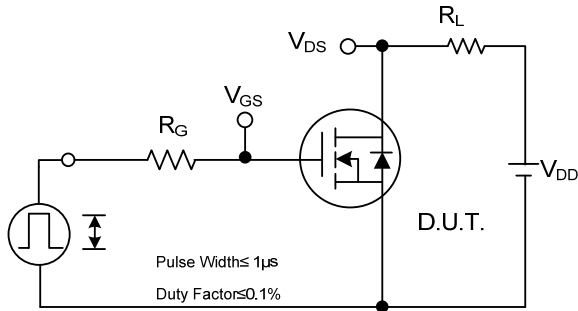


Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

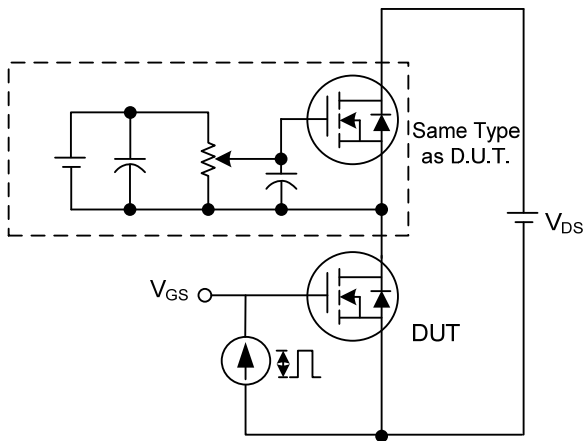
■ TEST CIRCUITS AND WAVEFORMS



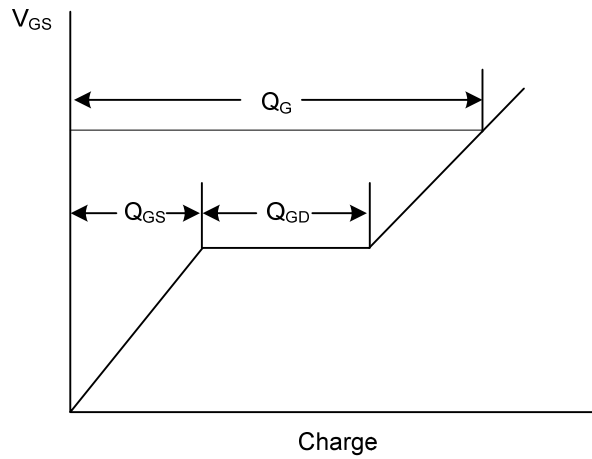
Switching Test Circuit



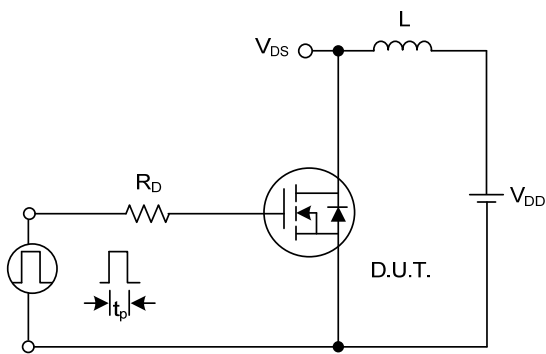
Switching Waveforms



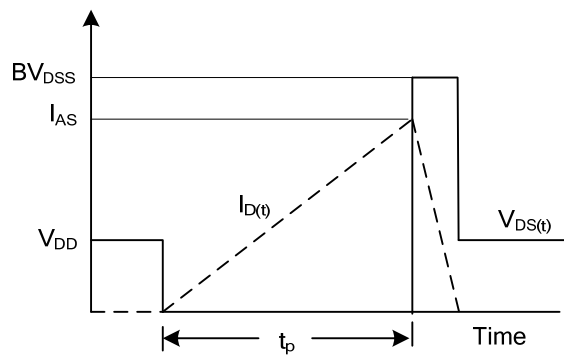
Gate Charge Test Circuit



Gate Charge Waveform

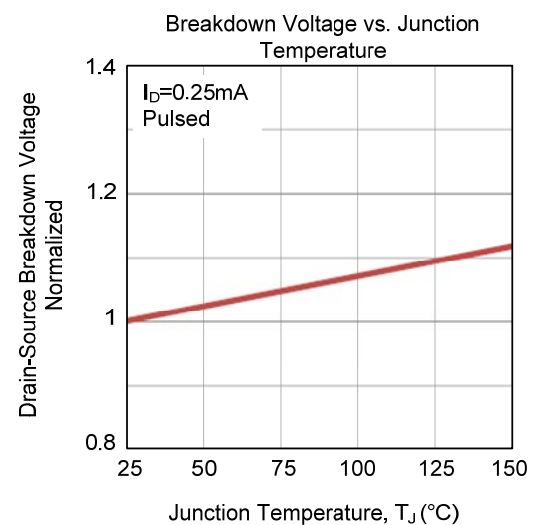
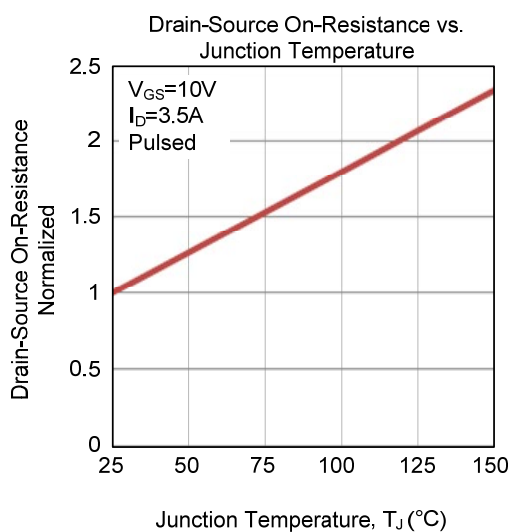
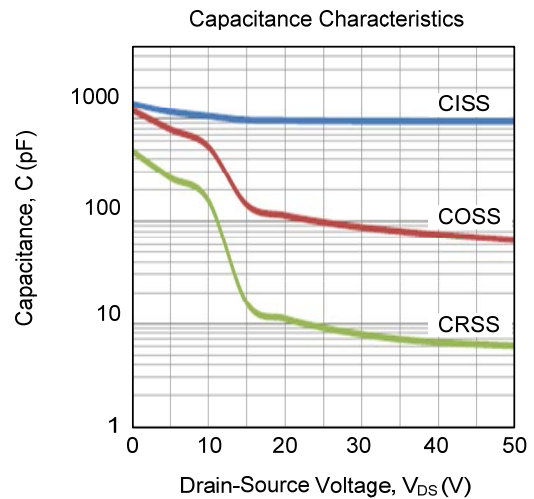
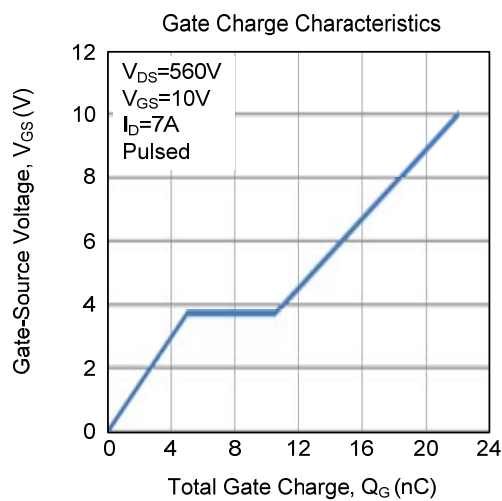
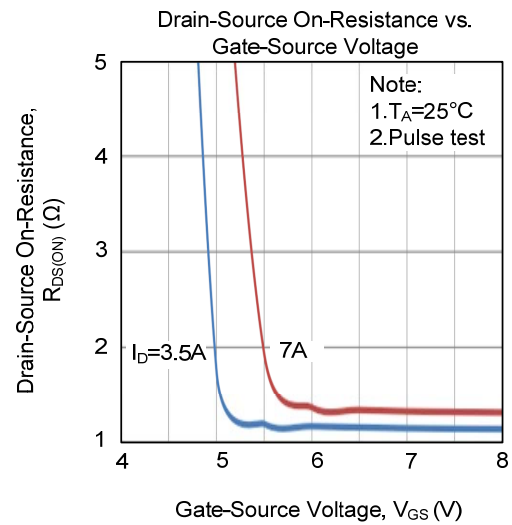
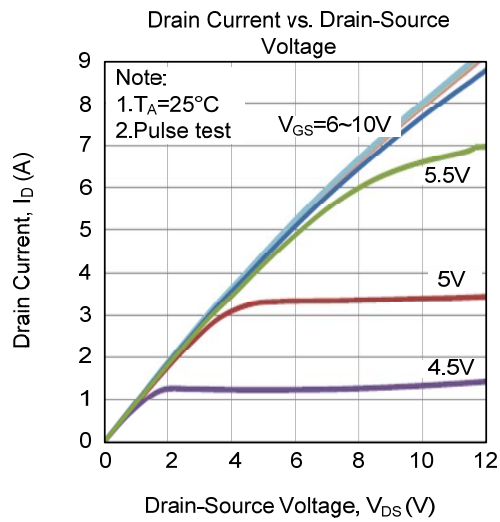


Unclamped Inductive Switching Test Circuit

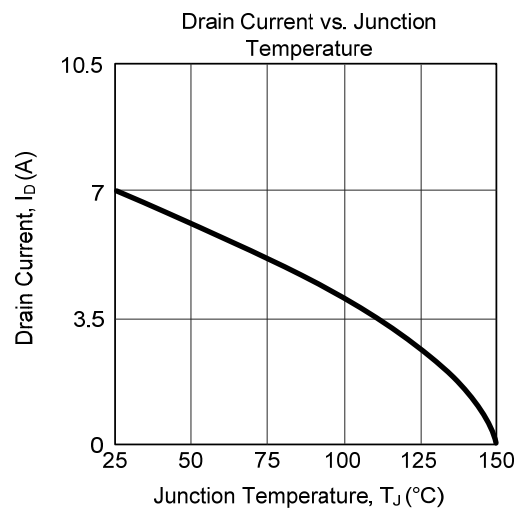
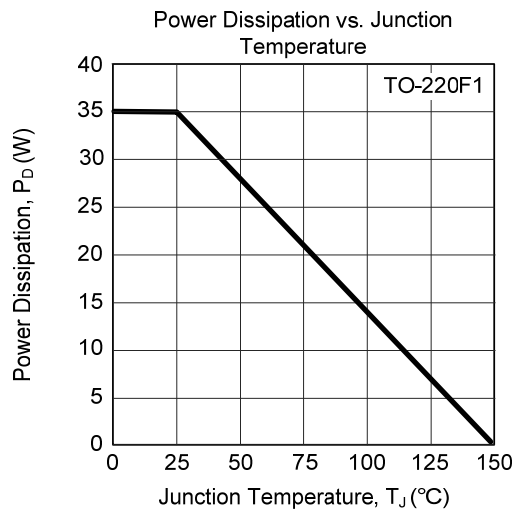
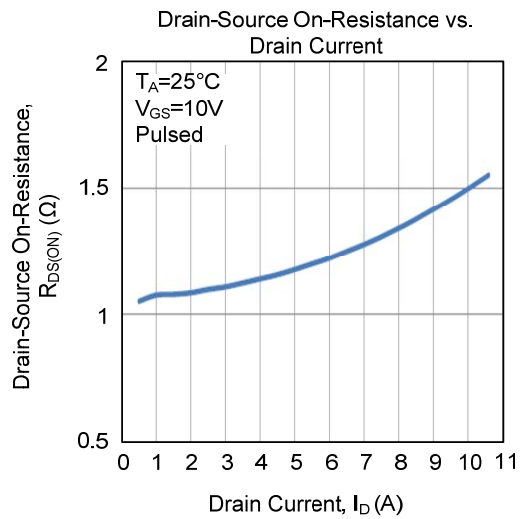
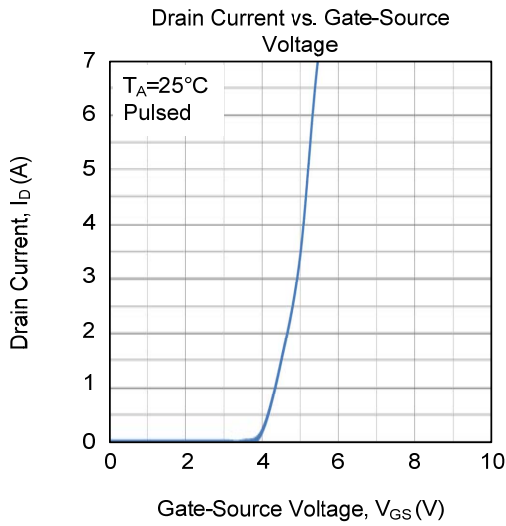
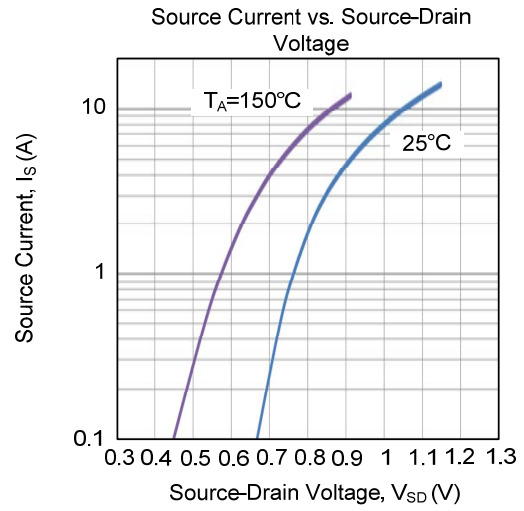
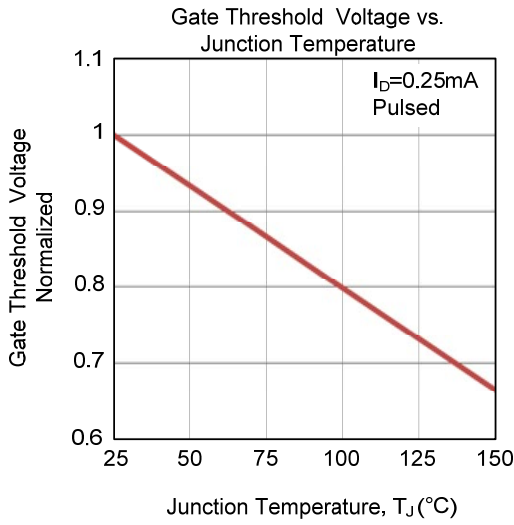


Unclamped Inductive Switching Waveforms

## TYPICAL CHARACTERISTICS

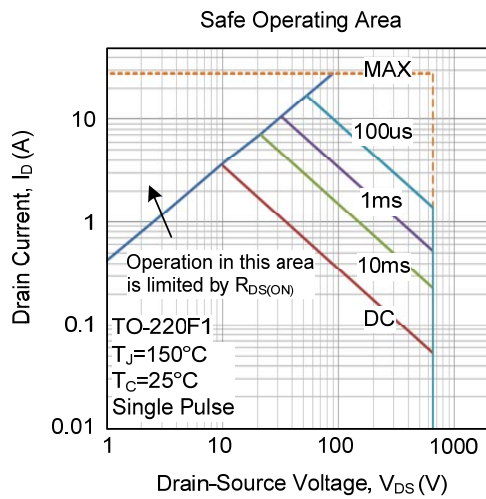


■ TYPICAL CHARACTERISTICS (Cont.)





■ TYPICAL CHARACTERISTICS (Cont.)



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