



## Features

- Low on-resistance
- ESD protected gate up to 2kV HBM
- High-speed switching
- Drive circuits can be simple
- Parallel use is easy

## Shipping Quantity

- 3000pcs / Tape & Reel

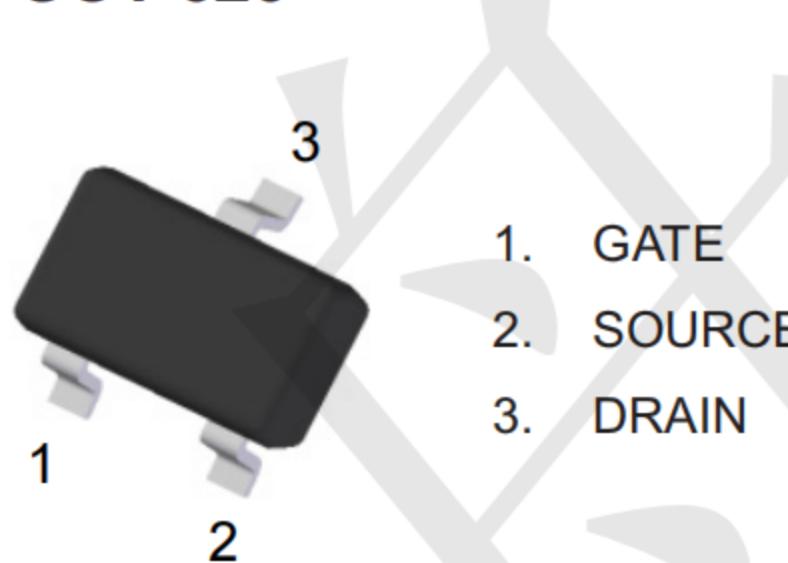
## Typical Applications

- N-channel enhancement mode effect transistor
- Switching application

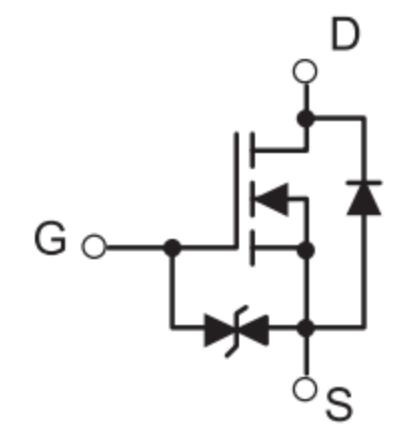
## Mechanical Data

- Case: SOT-323
- Molding Compound, UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin Plated Leads, Solderable Per MIL-STD-202, Method 208

SOT-323



## Circuit Diagram



N-MOS

**Marking: 72K c**

**Absolute Maximum Ratings** (Tamb=25°C unless otherwise specified)

Parameter	Symbol	Value	Units
Drain-Source Voltage	V <sub>DSS</sub>	60	V
Gate -Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current	I <sub>D</sub>	300	mA
Pulsed Drain Current ( $t_p = 10\mu s$ )	I <sub>DM</sub>	2000	mA
Single Pulse Avalanche Energy <sup>2</sup>	E <sub>AS</sub>	0.11	mJ
Power Dissipation	P <sub>D</sub>	0.25	W
SOT-323			



TECH PUBLIC

台舟电子

TPM7002EC3

## N-Channel Enhancement Mode MOSFET

www.sot23.com.tw

## Thermal Characteristics

Parameter	Symbol	Limits	Unit
Thermal Resistance Junction to Ambient Air	$R_{\theta JA}$	500	
Thermal Resistance Junction to Lead	$R_{\theta JL}$	313	$^{\circ}\text{C}/\text{W}$
Thermal Resistance Junction to Case	$R_{\theta JC}$	261	
Operating Junction Temperature Range	$T_J$	-55 to +150	$^{\circ}\text{C}$
Storage Temperature Range	$T_{STG}$	-55 to +150	$^{\circ}\text{C}$

## Electrical Characteristics (TA=25°C unless otherwise specified)

Symbol	Parameter	Test conditions	MIN	TYP	MAX	UNIT
<b>OFF Characteristics</b>						
$V_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	60	-	-	V
$I_{DSS}$	Drain to Source Leakage Current	$V_{DS} = 60\text{V}, V_{GS} = 0\text{V}$	-	-	1	$\mu\text{A}$
$I_{GSS}$	Gate-body Leakage	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	-	-	$\pm 10$	$\mu\text{A}$
<b>ON Characteristics</b>						
$R_{DS(ON)}$	Drain-Source On-resistance *1	$V_{GS} = 5\text{V}, I_D = 0.05\text{A}$	-	1.5	3	$\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 0.5\text{A}$	-	1.8	4	
		$V_{GS} = 10\text{V}, I_D = 0.5\text{A}$	-	1.45	2.5	
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	1.5	2.5	V
$R_G$	Gate Resistance	$V_{GS} = 0\text{V}, f = 1.0\text{MHz}$	-	39	-	$\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}$ $V_{DS} = 20\text{V}$ $f = 1.0\text{MHz}$	-	47.2	-	pF
$C_{oss}$	Output Capacitance		-	7.3	-	
$C_{rss}$	Reverse Transfer Capacitance		-	4.7	-	
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time *3	$V_{DD} = 30\text{V}, I_D = 0.2\text{A}$ $V_{GS} = 10\text{V}, R_G = 25\Omega$ $R_L = 150\Omega$	-	6	-	nS
$t_r$	Turn-on Rise Time *3		-	5	-	
$t_{d(off)}$	Turn-Off Delay Time *3		-	25	-	
$t_f$	Turn-Off Fall Time *3		-	15	-	
$Q_G$	Total Gate-Charge	$V_{DS} = 10\text{V}$ $V_{GS} = 4.5\text{V}$ $I_D = 0.2\text{A}$	-	0.44	-	nC
$Q_{GS}$	Gate to Source Charge		-	0.14	-	nC
$Q_{GD}$	Gate to Drain (Miller) Charge		-	0.2	-	nC
<b>Source-Drain Diode Characteristics</b>						
$V_{SD}$	Diode Forward Voltage *1	$I_S = 0.3\text{A}, V_{GS} = 0\text{V}$	-	0.85	1.2	V
$I_S$	Diode Continuous Forward Current	$T_C = 25^{\circ}\text{C}$	-	-	0.3	A

## N-Channel Enhancement Mode MOSFET

[www.sot23.com.tw](http://www.sot23.com.tw)

### Typical Performance Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise Specified)

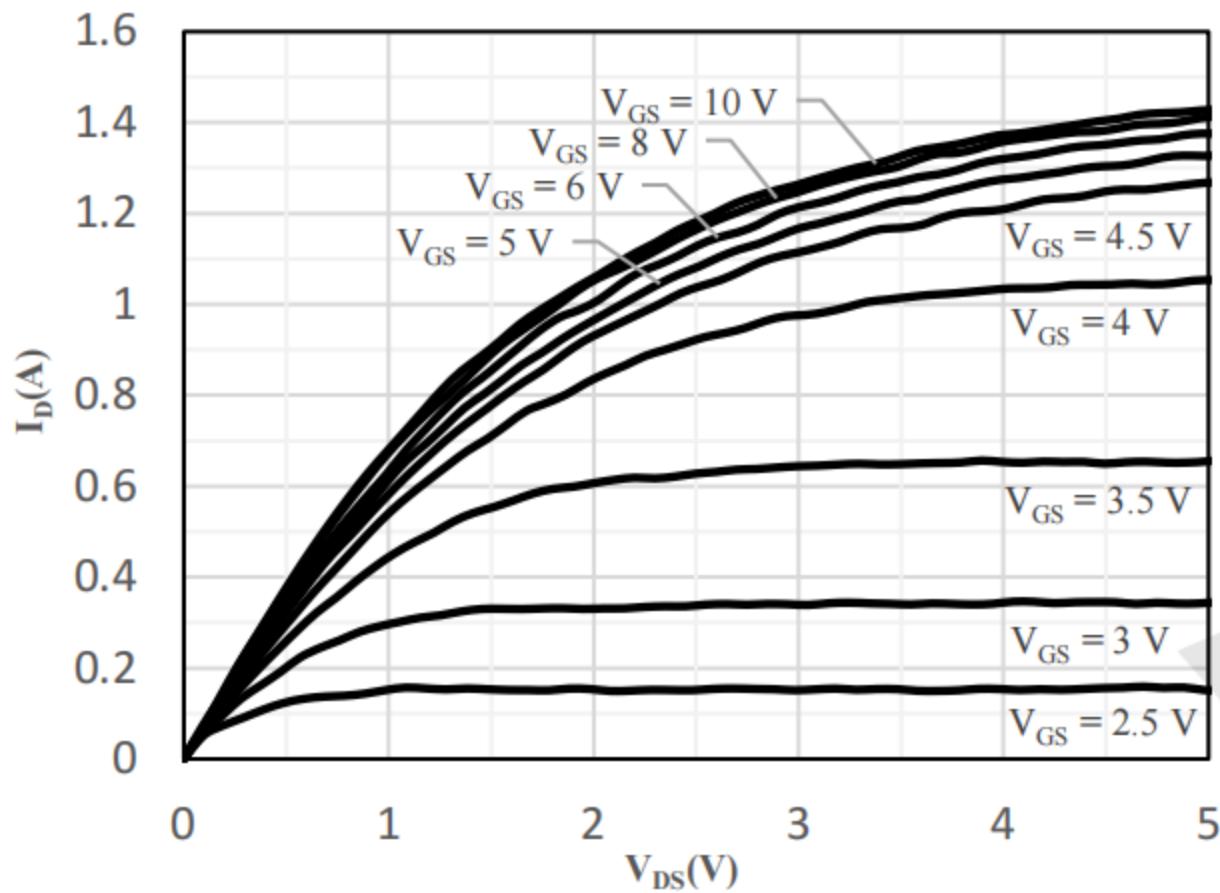


Fig 1 Typical Output Characteristics

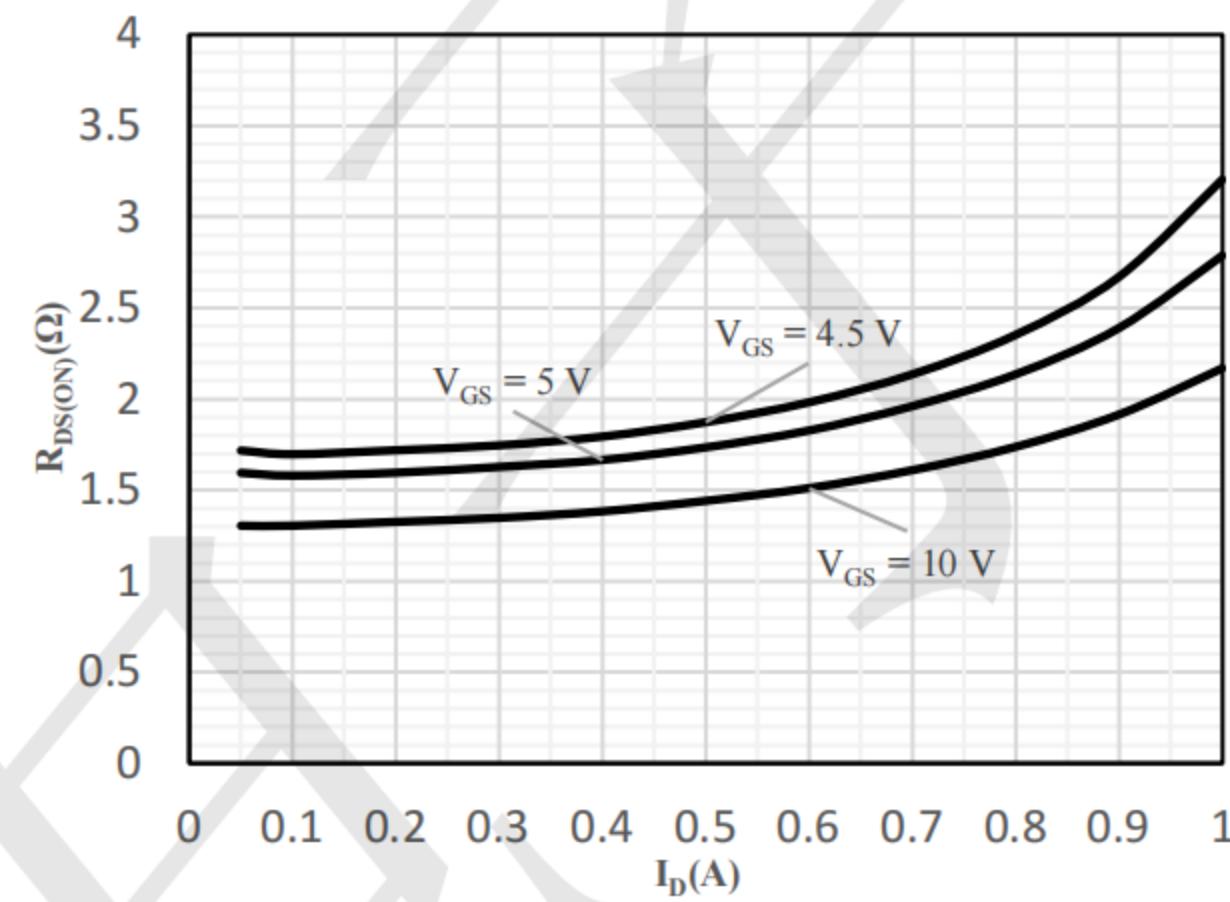


Fig 2 On-Resistance vs. Drain Current  
and Gate Voltage

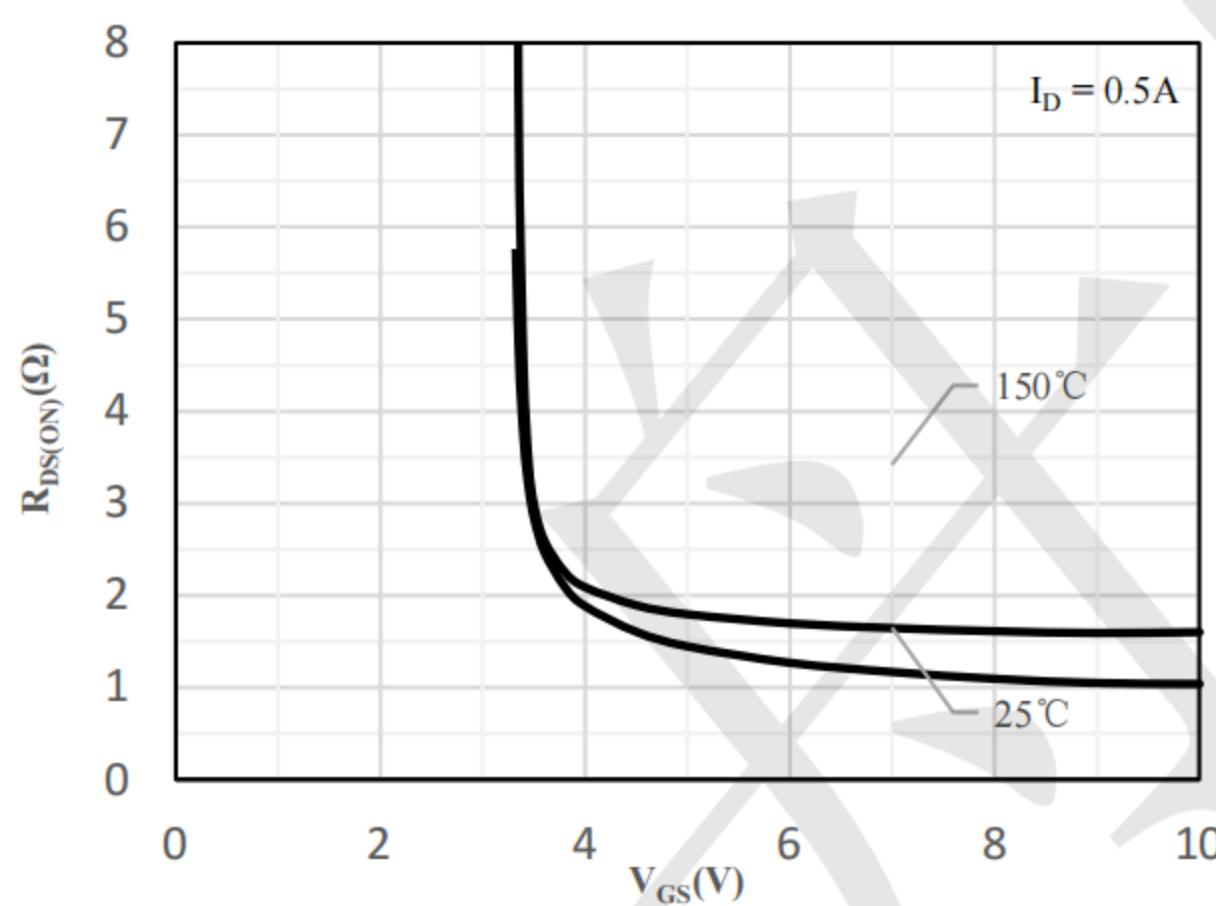


Fig 3 On-Resistance vs. Gate-Source Voltage

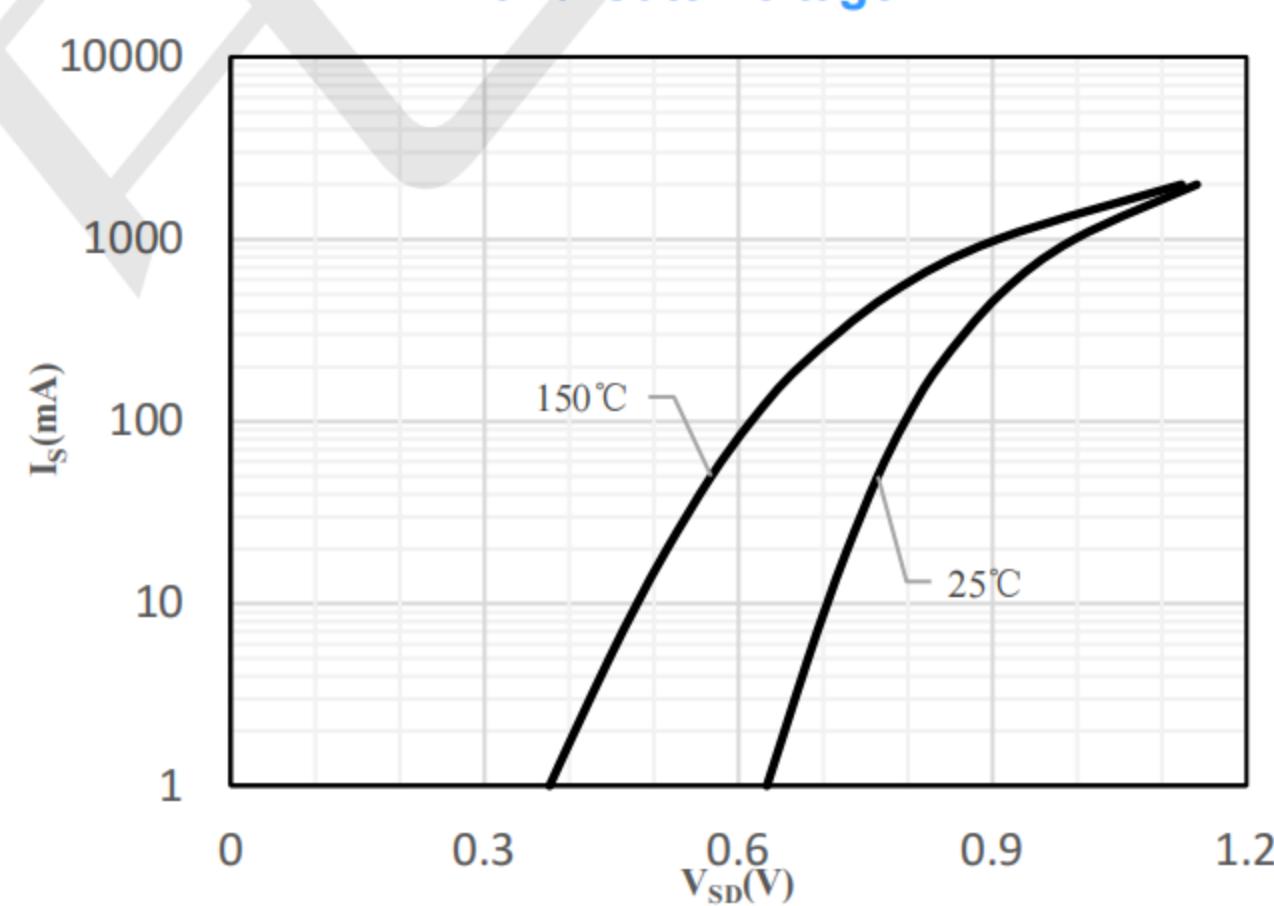


Fig 4 Body-Diode Characteristics

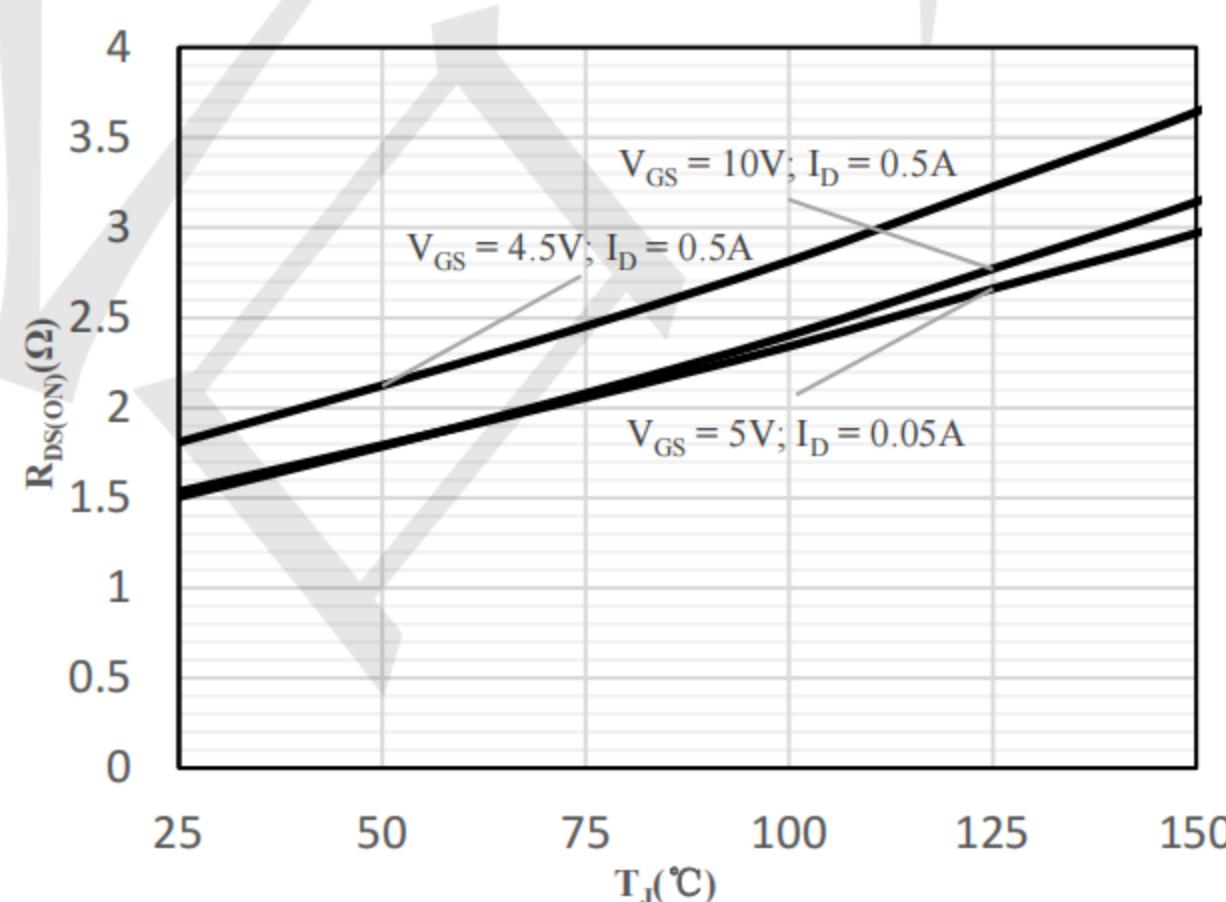


Fig 5 On-Resistance vs. Junction Temperature

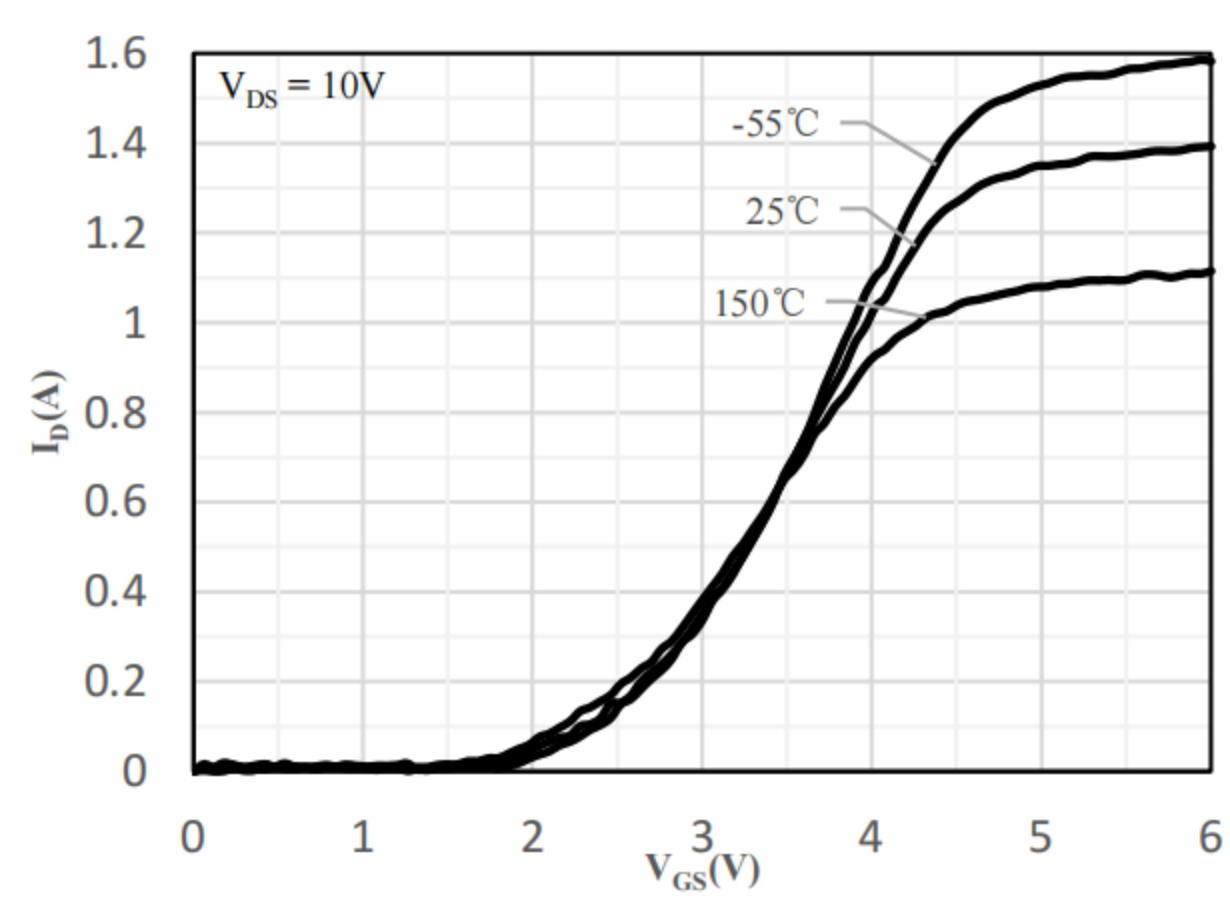


Fig 6 Transfer Characteristics

## N-Channel Enhancement Mode MOSFET

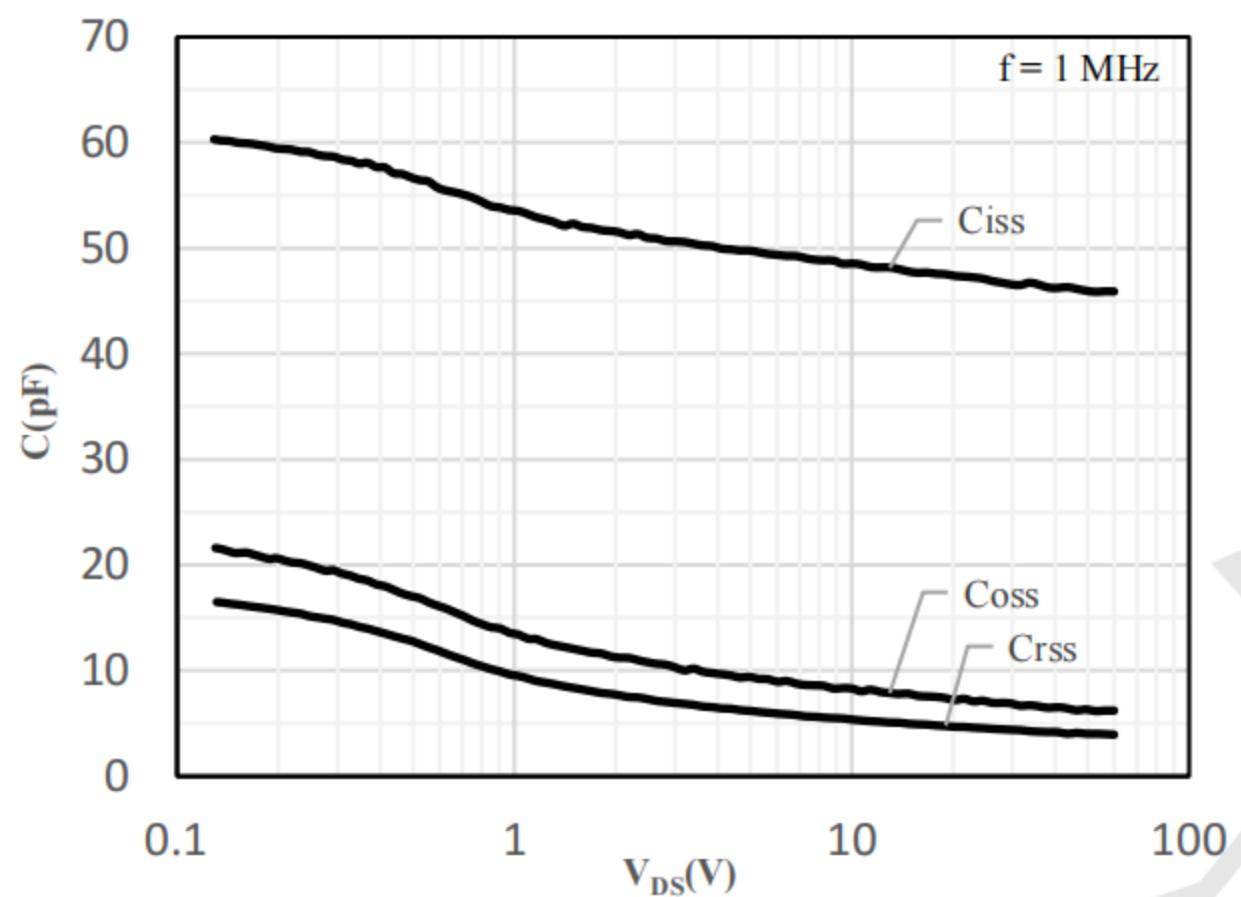
[www.sot23.com.tw](http://www.sot23.com.tw)Typical Performance Characteristics ( $T_A=25^\circ\text{C}$  unless otherwise Specified)

Fig 7 Capacitance Characteristics

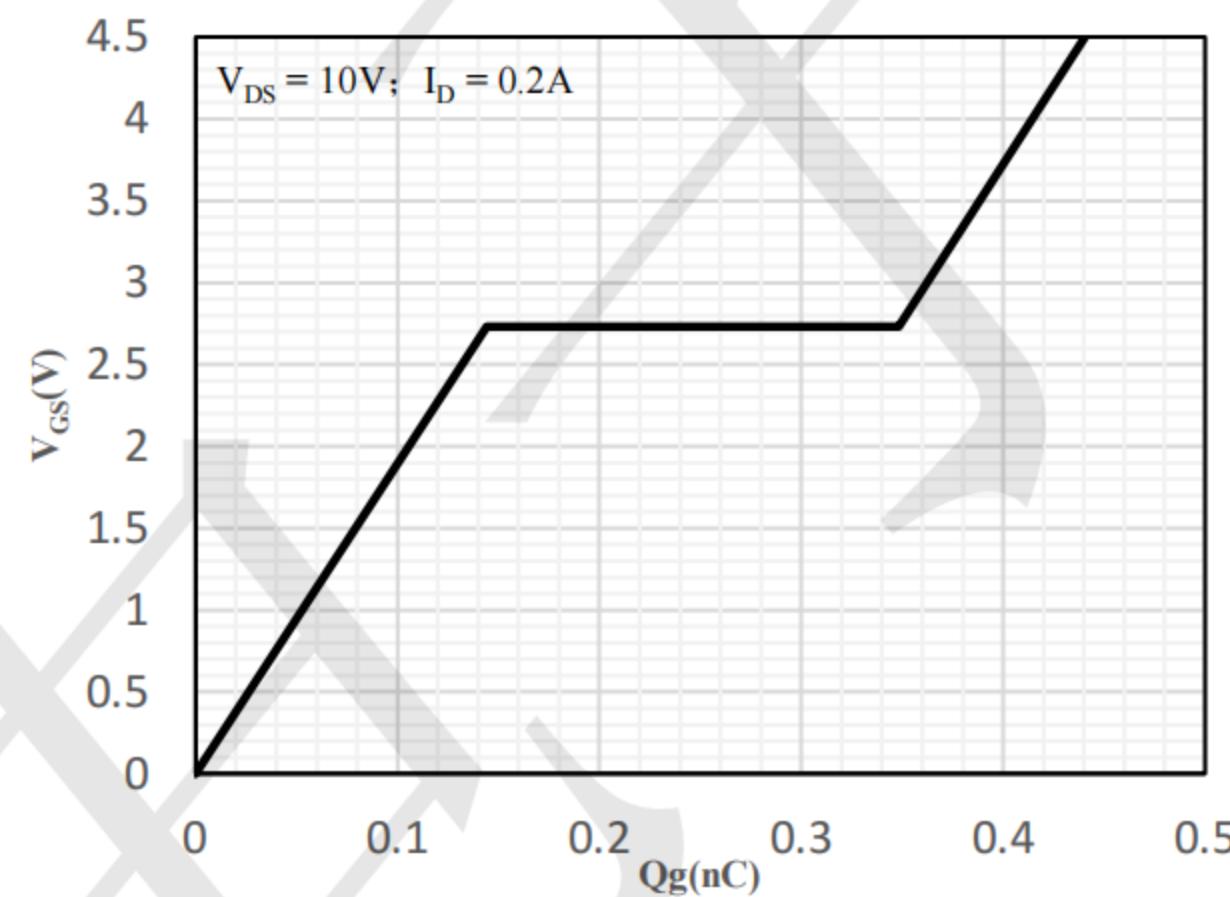


Fig 8 Gate-Charge Characteristics

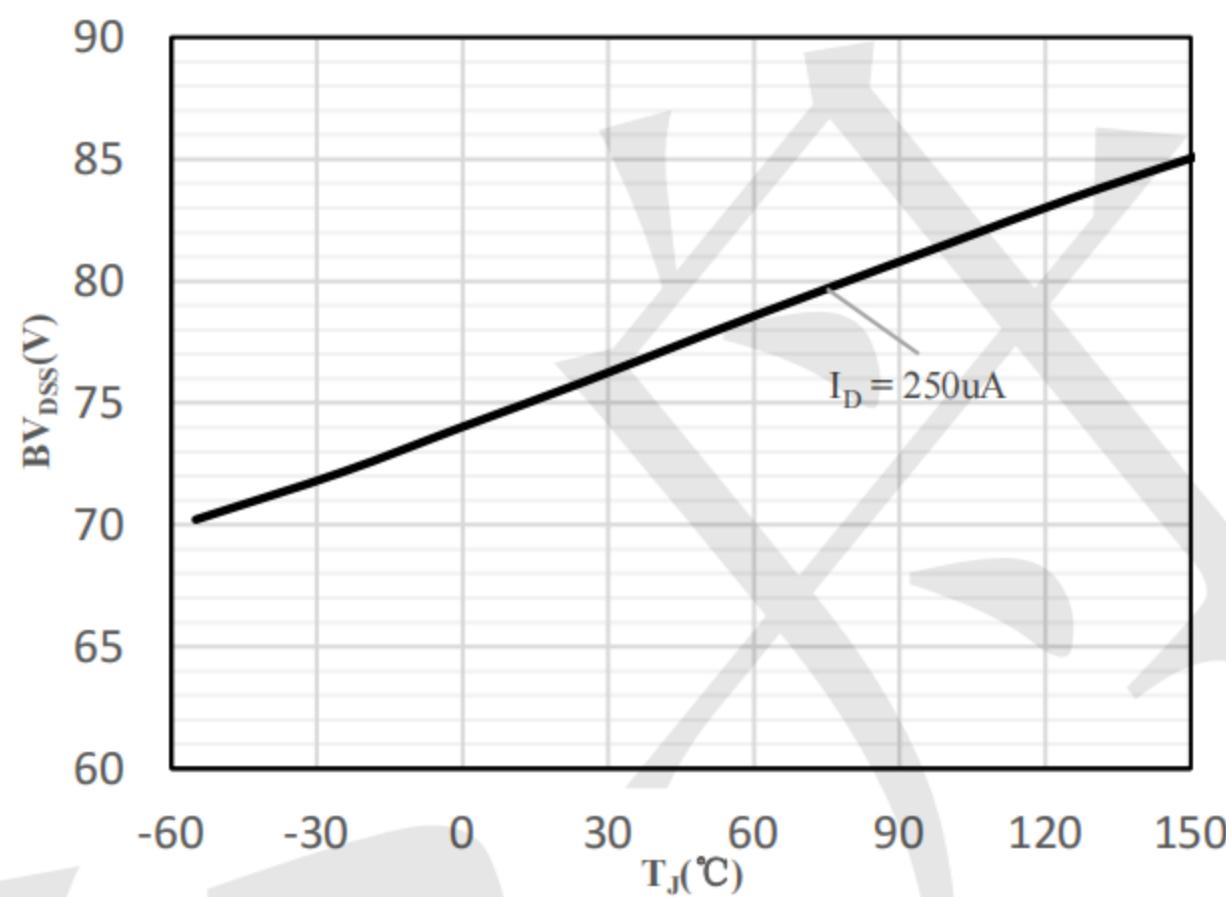
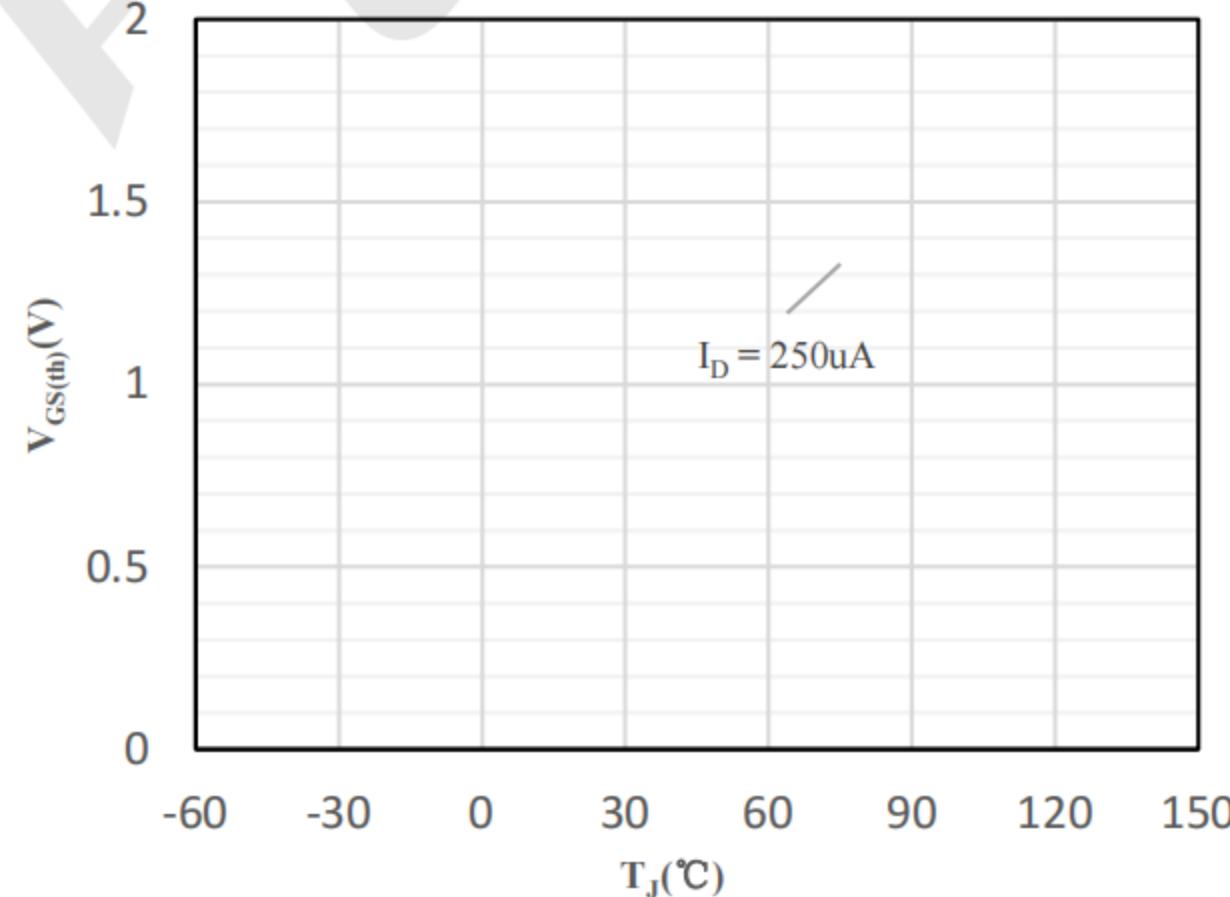


Fig 9 Breakdown Voltage vs. Junction Temperature

Fig 10  $V_{GS(\text{th})}$  vs. Junction Temperature



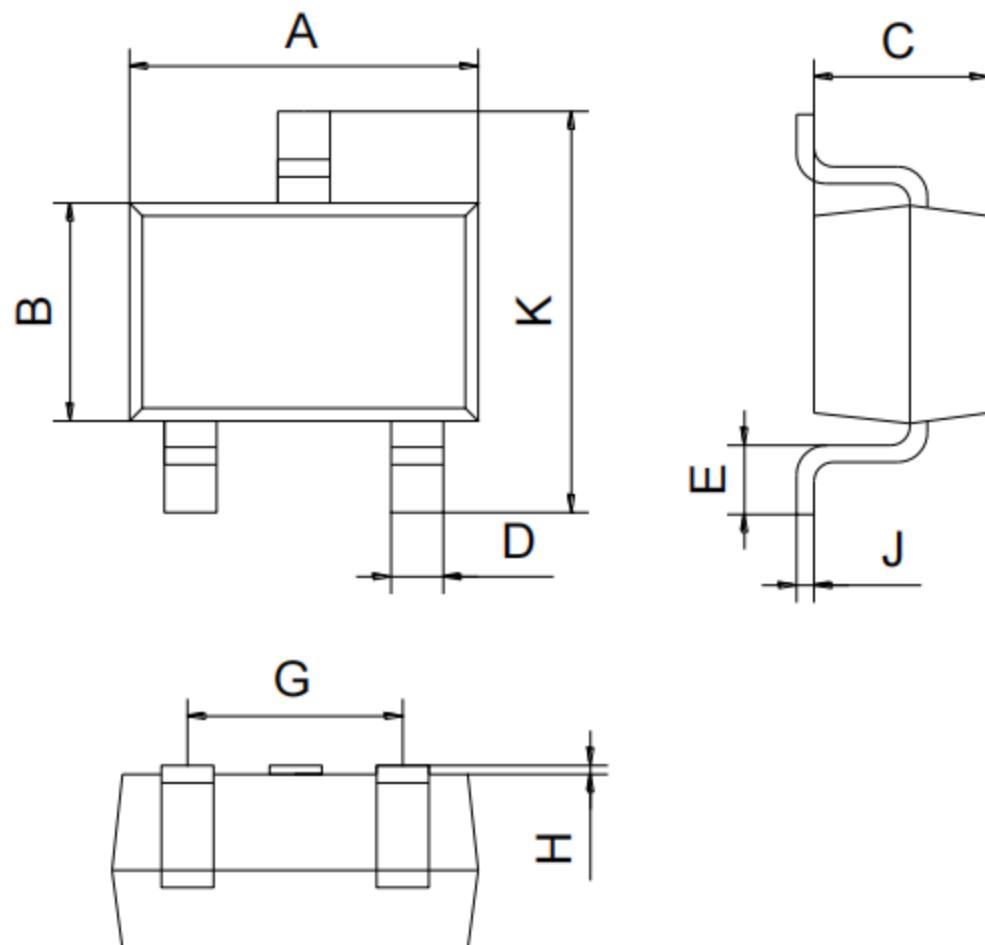
**TECH PUBLIC**  
台舟电子

**TPM7002EC3**

**N-Channel Enhancement Mode MOSFET**

[www.sot23.com.tw](http://www.sot23.com.tw)

**Outline Drawing - SOT323(unit: mm)**



SOT-323		
Dimension	Min.	Max.
A	2.00	2.20
B	1.15	1.35
C	0.90	1.10
D	0.15	0.35
E	0.25	0.40
G	1.20	1.40
H	0.02	0.10
J	0.05	0.15
K	2.20	2.40

**Mounting Pad Layout-SOT323(unit: mm)**

