

**UTC** UNISONIC TECHNOLOGIES CO., LTD

## 8050S

## NPN SILICON TRANSISTOR

# LOW VOLTAGE HIGH **CURRENT SMALL SIGNAL** NPN TRANSISTOR

#### DESCRIPTION

The UTC 8050S is a low voltage high current small signal NPN transistor, designed for Class B push-pull audio amplifier and general purpose applications.

#### **FEATURES**

- \* Collector current up to 700mA
- \* Collector-Emitter voltage up to 20V
- \* Complementary to UTC 8550S

#### **ORDERING INFORMATION**

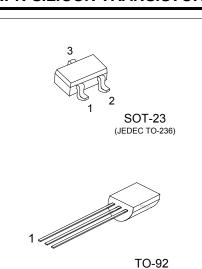
Ordering Number		Deelvere	Pin	Assignn	Deeking		
Lead Free	Halogen-Free	Package	1	2	3	Packing	
8050SL-x-AE3-R	8050SG-x-AE3-R	SOT-23	В	Е	С	Tape Reel	
8050SL-x-T92-B	8050SG-x-T92-B	TO-92	Е	С	В	Таре Вох	
8050SL-x-T92-K	8050SG-x-T92-K	TO-92	Е	С	В	Bulk	
Noto: Din Assignment: D: Dess	E: Emitter C: Cellecter						

Note: Pin Assignment: B: Base E: Emitter C: Collector

8050SG-x-AE3-R (1)Packing Type (2)Package Type (3)Rank (4)Green Package	<ul> <li>(1) R: Tape Reel, B: Tape Box, K: Bulk</li> <li>(2) AE3: SOT-23, T92: TO-92</li> <li>(3) x: refer to Classification of h<sub>FE2</sub></li> <li>(4) G: Halogen Free and Lead Free L: Lead Free</li> </ul>
(4)Green Package	(4) G: Halogen Free and Lead Free, L: Lead Free

#### MARKING

SOT-23	TO-92		
☐ D9_D□ ☐ ☐ ☐ C: Lead Free G: Halogen Free	UTC 8050S□ G: Halogen Free Rank ← □ □□□ 1 Date Code		



### ■ ABSOLUTE MAXIMUM RATING (T<sub>A</sub>=25°C, unless otherwise specified )

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Base Voltage		V <sub>CBO</sub>	30	V
Collector-Emitter Voltage		V <sub>CEO</sub>	20	V
Emitter-Base Voltage		V <sub>EBO</sub>	5	V
Collector Current		Ι <sub>C</sub>	700	mA
Collector Dissipation (T <sub>A</sub> =25°C)	SOT-23	D	350	mW
	TO-92	P <sub>C</sub>	1	W
Junction Temperature		TJ	+150	°C
Storage Temperature		T <sub>STG</sub>	-40 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### ■ **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	$I_{\rm C} = 100 \mu A, I_{\rm E} = 0$	30			V
Collector-Emitter Breakdown Voltage	$BV_{CEO}$	I <sub>C</sub> = 1mA, I <sub>B</sub> = 0	20			V
Emitter-Base Breakdown Voltage	$BV_{EBO}$	I <sub>E</sub> = 100μΑ, I <sub>C</sub> =0	5			V
Collector Cut-Off Current	I <sub>CBO</sub>	$V_{CB} = 30V, I_{E} = 0$			1	uA
Emitter Cut-Off Current	I <sub>EBO</sub>	$V_{EB} = 5V, I_{C} = 0$			100	nA
	h <sub>FE1</sub>	V <sub>CE</sub> = 1V, I <sub>C</sub> = 1mA	100			
DC Current Gain (note)	h <sub>FE2</sub>	V <sub>CE</sub> = 1V, I <sub>C</sub> = 150 mA	120		400	
	h <sub>FE3</sub>	V <sub>CE</sub> = 1V, I <sub>C</sub> = 500mA	40			
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> = 500mA, I <sub>B</sub> = 50mA			0.5	V
Base-Emitter Saturation Voltage		I <sub>C</sub> = 500mA, I <sub>B</sub> = 50mA			1.2	V
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	V <sub>CE</sub> = 1V, I <sub>C</sub> = 10mA			1.0	V
Current Gain Bandwidth Product	f⊤	V <sub>CE</sub> = 10V, I <sub>C</sub> = 50mA	100			MHz
Output Capacitance	Cob	V <sub>CB</sub> = 10V, I <sub>E</sub> = 0, f = 1MHz		9.0		pF

### CLASSIFICATION OF h<sub>FE2</sub>

RANK	С	D	E
RANGE	120-200	160-300	280-400



# 8050S

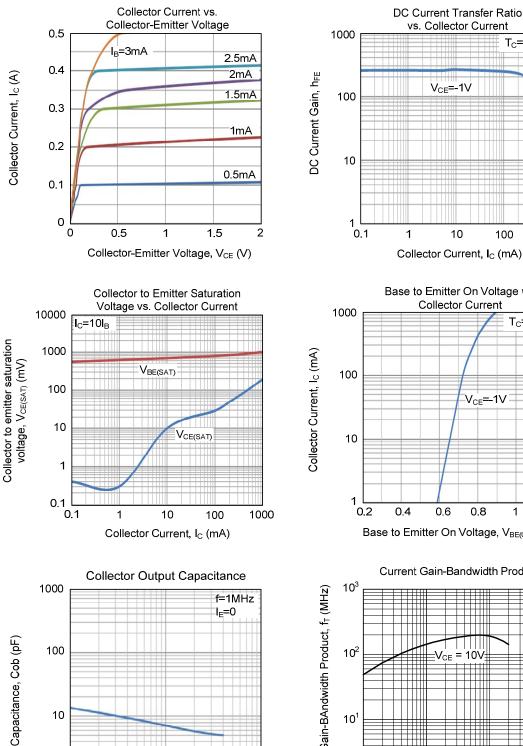
## NPN SILICON TRANSISTOR

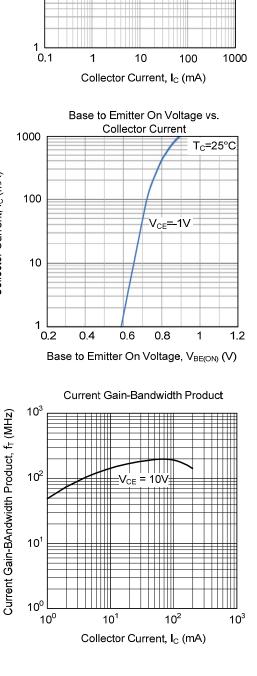
T<sub>C</sub>=25°C

vs. Collector Current

V<sub>CE</sub>=-1V

### **TYPICAL CHARACTERISTICS**







10

Collector-Base Voltage (V)

100

10

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